RT3TBBM

Composite Transistor With Resistor For Switching Application Silicon Epitaxial Type

DESCRIPTION

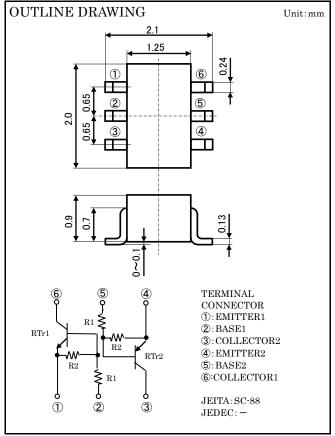
RT3TBBM is composite transistor built with RT1N231 chip and RT1P231 chip in SC-88 package.

FEATURE

Built-in bias resistor (R1=2.2k Ω , R2=2.2k Ω) Mini package for easy mounting

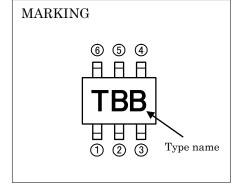
APPLICATION

Inverted circuit, Switching circuit, Interface circuit, Driver circuit



MAXIMUM RATING (Ta= 25° C) (RTr1_NPN, RTr2_PNP)

SYMBOL	PARAMETER	RATING	UNIT	
Vcbo	Collector to Base voltage	50	V	
Vebo	Emitter to Base voltage	10	V	
VCEO	Collector to Emitter voltage	50	V	
VIN	Input voltage	12	V	
I_{C}	Collector current	100	mA	
Icm	Peak Collector current	200	mA	
P_{T}	Total dissipation	200	mW	
Tj	Junction temperature	+150	°C	
$T_{ m stg}$	Storage temperature	-55~+150	°C	



*PNP built in transistor of "-"sign is abbreviation.

ELECTRICAL CHARACTERISTICS (Ta=25°C) (RTr1_NPN, RTr2_PNP)

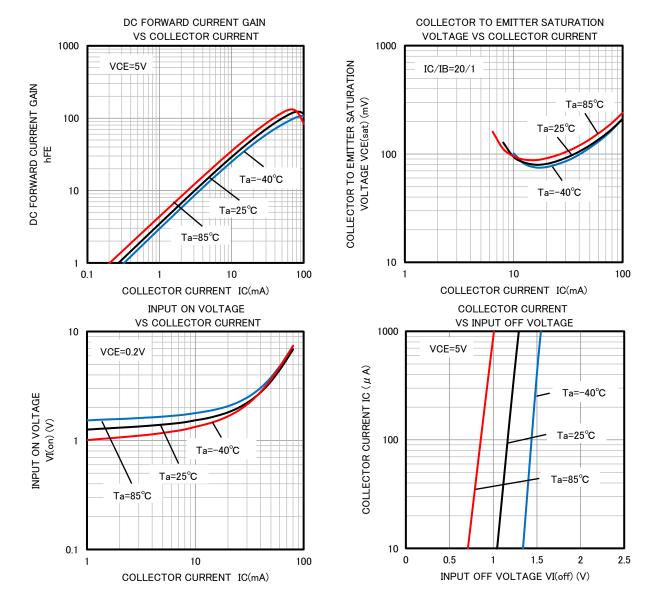
Symbol	Parameter	Test conditions		Limits			TT :
				Min	Тур	Max	Unit
V(BR)CEO	Collector to Emitter breakdown voltage	$I_{\rm C}$ =100 μ A, $R_{\rm BE}$ = ∞		50	_	_	V
Icbo	Collector cut off current V_{CB} =50V, I_{E} =0		_	_	0.1	μΑ	
IEBO	nitter cut off current V_{EB} =5V, I_{C} =0		850	1140	1650	μΑ	
hfe	DC forward current gain	V _{CE} =5V, I _C =20mA		20	_	_	_
V _{CE} (sat)	Collector to Emitter saturation voltage Ic=10mA, I _B =0.5mA		I	_	0.3	V	
VI(ON)	Input on voltage V_{CE} =0.2V, I_{C} =5mA		ı	1.3	2.2	V	
V _{I(OFF)}	Input off voltage	$V_{CE}=5V, I_{C}=100 \muA$		0.7	1.1	_	V
R_1	Input resistor	_		1.5	2.2	2.9	$k\Omega$
R ₂ /R ₁	Resistor ratio	_		0.8	1.0	1.2	_
f_{T}	Gain band width product	V _{CE} =6V, I _E =10mA	RTr1	-	200	_	m MHz
			RTr2	-	150	_	мпд

XPNP built in transistor of "−"sign is abbreviation.

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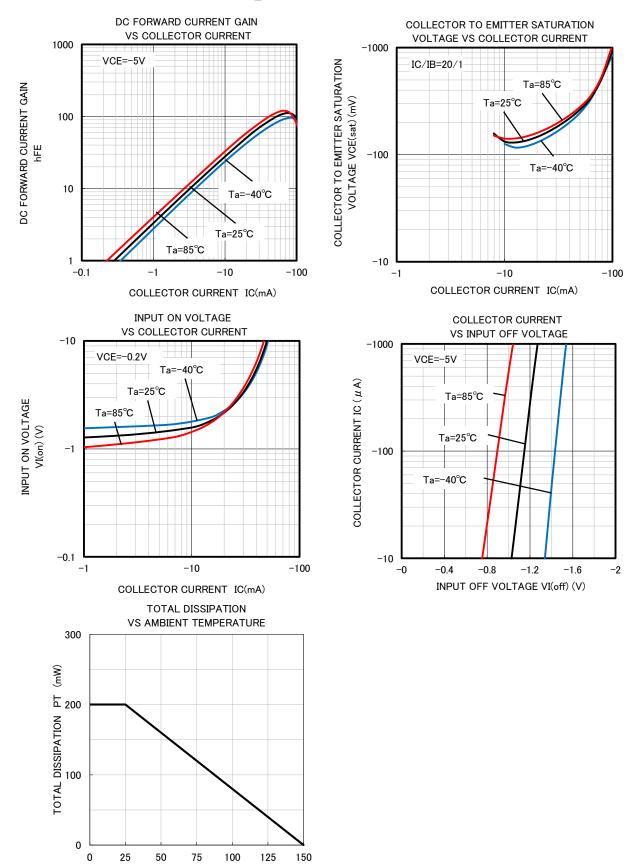
TYPICAL CHARACTERISTICS (RTr1_NPN)



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TYPICAL CHARACTERISTICS (RTr 2_PNP)



AMBIENT TEMPERATURE Ta (°C)

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