# RT2N21M

Composite Transistor With Resistor For Switching Application Silicon NPN Epitaxial Type

#### DESCRIPTION

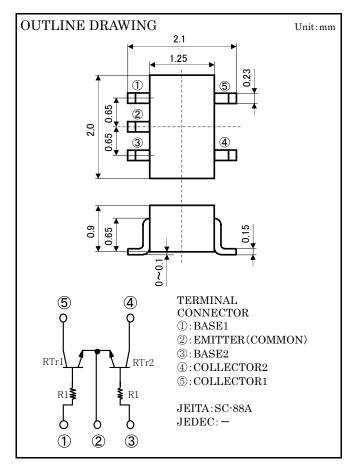
RT2N21M is composite transistor with built-in bias resistor.

### FEATURE

Built-in bias resistor (R1=10k $\Omega$ ) Mini package for easy mounting

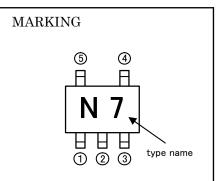
#### APPLICATION

Inverted circuit, Switching circuit, Interface circuit, Driver circuit



### MAXIMUM RATING(Ta=25°C)(RTr1, RTr2 COMMON)

SYMBOL	PARAMETER	RATING	UNIT
VCBO	Collector to Base voltage	50	V
Vebo	Emitter to Base voltage	6	V
VCEO	Collector to Emitter voltage	50	V
IC	Collector current	100	mA
ICM	Peak Collector current	200	mA
$P_{T}$	Total dissipation	200	mW
Tj	Junction temperature	+150	°C
$T_{stg}$	Storage temperature	-55~+150	°C

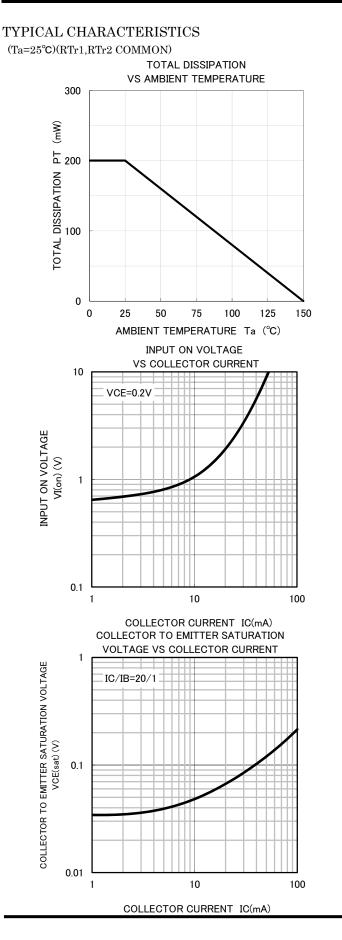


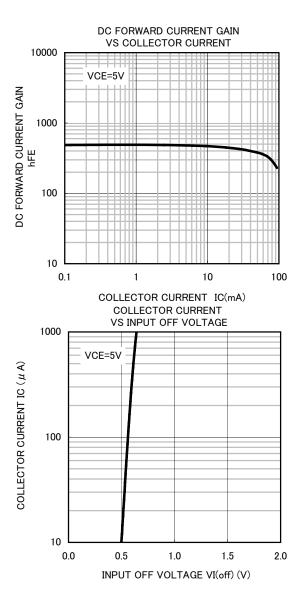
#### ELECTRICAL CHARACTERISTICS(Ta=25°C)(RTr1, RTr2 COMMON)

SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	UNII
V(BR)CEO	Collector to Emitter breakdown voltage	$I_C=100 \mu$ A, $R_{BE}=\infty$	50		_	V
ICBO	Collector cut off current	$V_{CB}$ =50V, $I_E$ =0	Ι		0.1	μA
IEBO	Emitter cut off current	$V_{EB}$ =5V, I <sub>C</sub> =0	1		0.1	μA
hfe	DC forward current gain	V <sub>CE</sub> =5V, I <sub>C</sub> =1mA	100	-	—	-
VCE(sat)	Collector to Emitter saturation voltage	Ic=10mA, IB=0.5mA	Ι	0.1	0.3	V
R1	Input resistor	-	7	10	13	kΩ
$f_{\mathrm{T}}$	Gain band width product	$V_{CE}=6V$ , $I_{E}=-10mA$		200	_	MHz

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