

RT1P241M-T150

Transistor With Resistor For Switching Application Silicon PNP Epitaxial Type

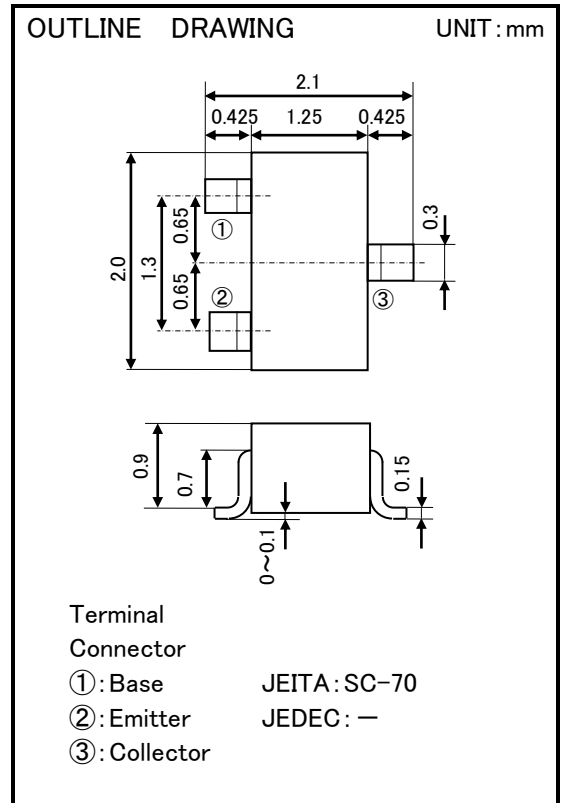
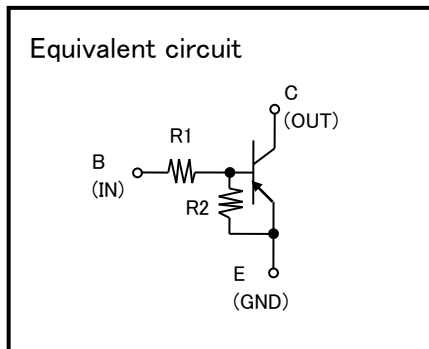
AEC-Q101 Compliance

FEATURE

- Built-in bias resistor ($R_1=22k\Omega, R_2=22k\Omega$)
- Mini package for easy mounting

APPLICATION

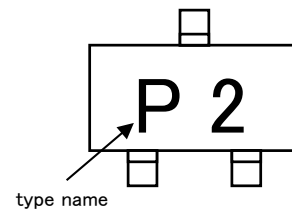
Inverted circuit, switching circuit, interface circuit, driver circuit.



MAXIMUM RATING (T_a=25°C)

SYMBOL	PARAMETER	RATING	UNIT
V_{CBO}	Collector to Base voltage	-50	V
V_{EBO}	Emitter to Base voltage	-10	V
V_{CEO}	Collector to Emitter voltage	-50	V
V_{IN}	Input voltage	-40	V
I_C	Collector current	-100	mA
I_{CM}	Peak Collector current	-200	mA
P_C	Collector dissipation	200	mW
T_j	Junction temperature	+150	°C
T_{stg}	Storage temperature	-55~+150	°C

MARKING



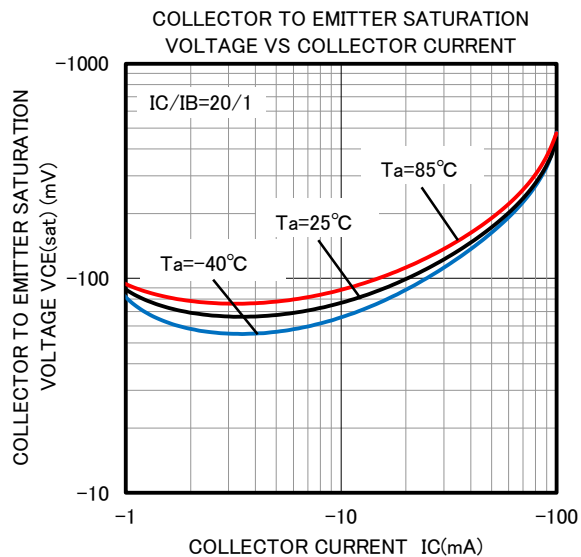
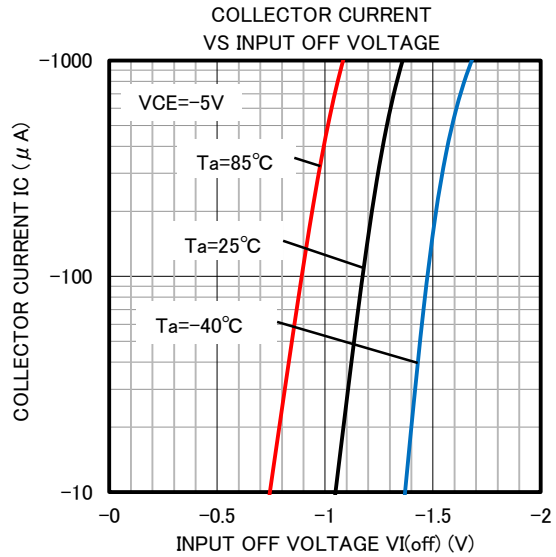
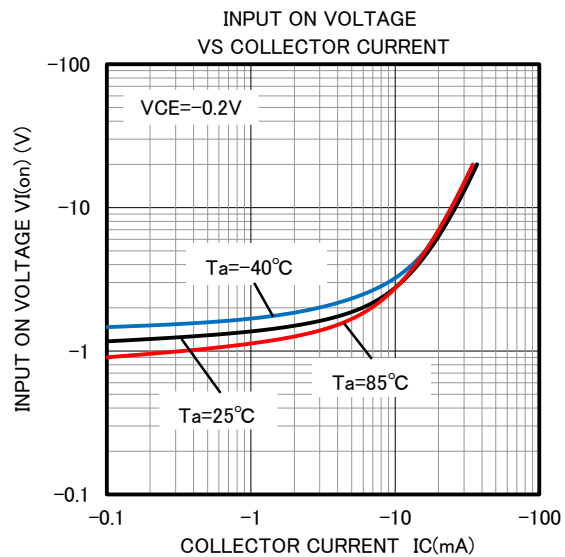
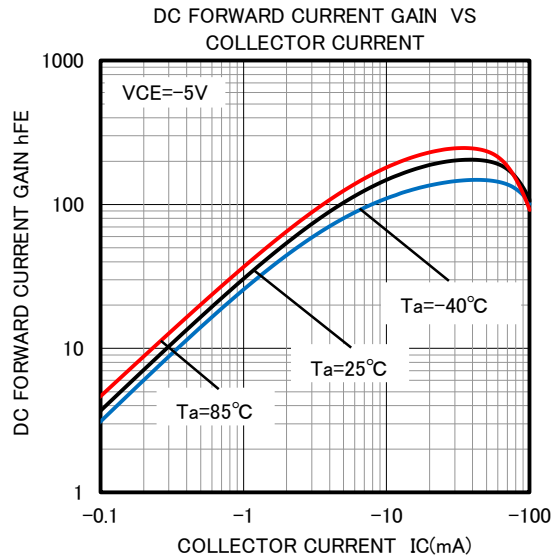
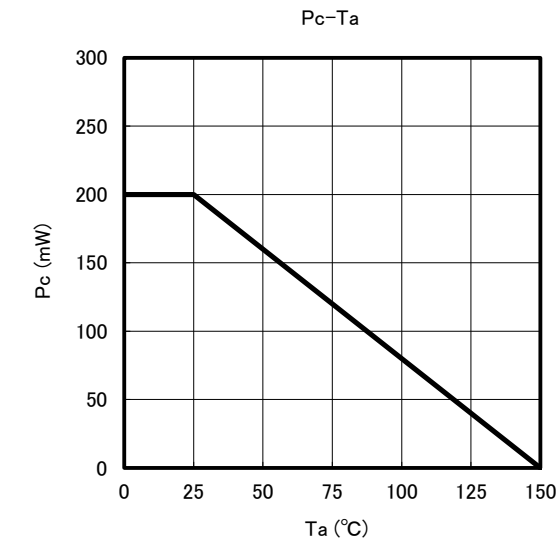
ELECTRICAL CHARACTERISTICS (T_a=25°C)

SYMBOL	PARAMETER	TEST CONDITION	LIMIT			UNIT
			MIN	TYP	MAX	
$V_{(BR)CEO}$	C to E breakdown voltage	$I_C=-100\mu A, R_{BE}=\infty$	-50	—	—	V
I_{CBO}	Collector cut off current	$V_{CB}=-50V, I_E=0$	—	—	-0.1	μA
I_{EBO}	Emitter cut off current	$V_{EB}=-5V, I_C=0$	-89	-113	-156	μA
h_{FE}	DC forward current gain	$V_{CE}=-5V, I_C=-5mA$	50	—	—	—
$V_{CE(sat)}$	C to E saturation voltage	$I_C=-10mA, I_B=-0.5mA$	—	-0.1	-0.3	V
$V_{I(ON)}$	Input on voltage	$V_{CE}=-0.2V, I_C=-5mA$	—	-1.8	-3.0	V
$V_{I(OFF)}$	Input off voltage	$V_{CE}=-5V, I_C=-100\mu A$	-0.8	-1.1	—	V
R1	Input resistor	—	16	22	28	$k\Omega$
R2/R1	Resistor ratio	—	0.9	1.0	1.1	—
f_T	Gain band width product	$V_{CE}=-6V, I_E=10mA$	—	150	—	MHz

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TYPICAL CHARACTERISTICS



Keep safety first in your circuit designs!

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