RT3PFFM

Composite Transistor With Resistor For Switching Application Silicon Epitaxial Type

DESCRIPTION

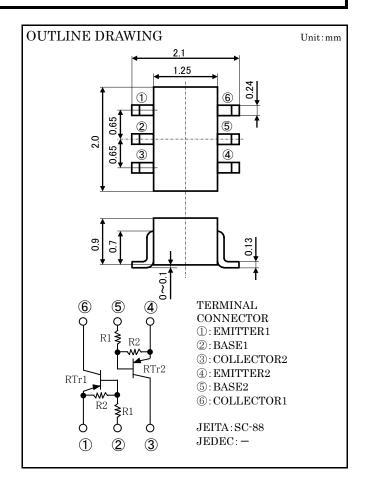
RT3PFFM is composite transistor built with two RT1P431 chips in SC-88 package.

FEATURE

Built-in bias resistor (R1=4.7k Ω , R2=4.7k Ω) Mini package for easy mounting

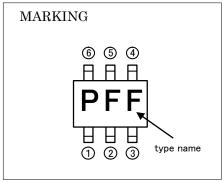
APPLICATION

Inverted circuit, Switching circuit, Interface circuit, Driver circuit



MAXIMUM RATING(Ta=25°C)(RTr1, RTr2 COMMON)

SYMBOL	PARAMETER	RATING	UNIT
Vcbo	Collector to Base voltage	-50	V
V_{EBO}	Emitter to Base voltage	-10	V
V_{CEO}	Collector to Emitter voltage	-50	V
V_{IN}	Input voltage	-30	V
I_{C}	Collector current	-100	mA
ICM	Peak Collector current	-200	mA
PT	Total dissipation	200	mW
Tj	Junction temperature	+150	္ဇ
$T_{ m stg}$	Storage temperature	-55~+150	°C



$ELECTRICAL\ CHARACTERISTICS (Ta=25°C) (RTr1,\ RTr2\ COMMON)$

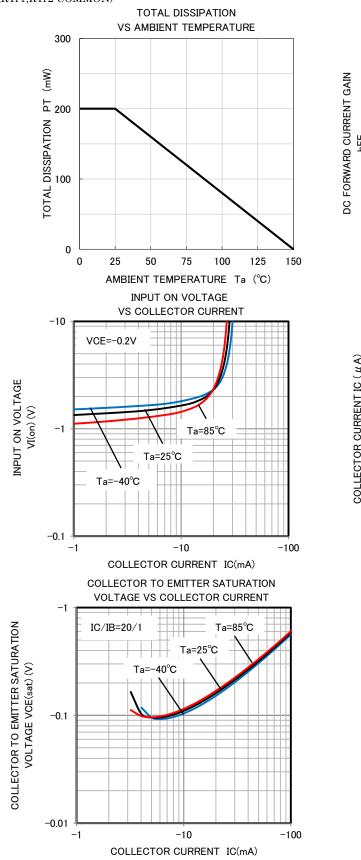
SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			TINITE
			MIN	TYP	MAX	UNIT
V(BR)CEO	Collector to Emitter breakdown voltage	$I_C=-100 \mu$ A, $R_{BE}=\infty$	-50	_	_	V
Icbo	Collector cut off current	V_{CB} =-50V, I_{E} =0	_	_	-0.1	μΑ
IEBO	Emitter cut off current	V_{EB} =-5 V , I_{C} =0	-399	-532	-771	μΑ
hfe	DC forward current gain	V_{CE} =-5V, I_{C} =-10mA	20	_	_	_
VCE(sat)	Collector to Emitter saturation voltage	I_{C} =-10mA, I_{B} =-0.5mA	_	-0.1	-0.3	V
$V_{\rm I(ON)}$	Input on voltage	V_{CE} =-0.2V, I_{C} =-5mA	_	-1.4	-2.3	V
$V_{\rm I(OFF)}$	Input off voltage	V_{CE} =-5 V , I_{C} =-100 μ A	-0.8	-1.1	_	V
R ₁	Input resistor	_	3.3	4.7	6.1	kΩ
R_2/R_1	Resistor ratio	_	0.8	1.0	1.2	_
fT	Gain band width product	V _{CE} =-6V, I _E =10mA	_	150	_	MHz

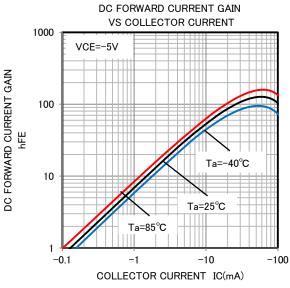
RT3PFFM

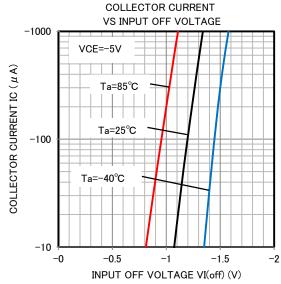
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TYPICAL CHARACTERISTICS

(RTr1,RTr2 COMMON)







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