# RT3NUUM

Composite Transistor With Resistor For Switching Application Silicon Epitaxial Type

### DESCRIPTION

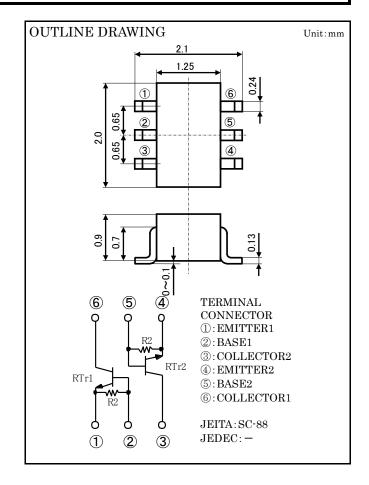
RT3NUUM is composite transistor built with two RT1N14B chips in SC-88 package.

### **FEATURE**

Built-in bias resistor (R2= $10k\Omega$ ) Mini package for easy mounting

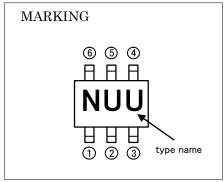
### APPLICATION

Inverted circuit, Switching circuit, Interface circuit, Driver circuit



## MAXIMUM RATING(Ta=25°C)(RTr1, RTr2 COMMON)

SYMBOL	PARAMETER	RATING	UNIT
Vcbo	Collector to Base voltage	50	V
$V_{\mathrm{EBO}}$	Emitter to Base voltage	6	V
$V_{\rm CEO}$	Collector to Emitter voltage	50	V
Ic	Collector current	100	mA
$I_{CM}$	Peak Collector current	200	mA
PT	Total dissipation	200	mW
$T_{\rm j}$	Junction temperature	+150	ပ္
$T_{\mathrm{stg}}$	Storage temperature	-55~+150	လူ



# $ELECTRICAL\ CHARACTERISTICS (Ta=25°C) (RTr1,\ RTr2\ COMMON)$

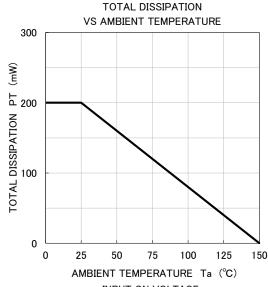
SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			TINIIM
			MIN	TYP	MAX	UNIT
V(BR)CEO	Collector to Emitter breakdown voltage	$I_C=100 \mu$ A, $R_{BE}=\infty$	50	_	_	V
ICBO	Collector cut off current	$V_{CB}$ =50V, $I_{E}$ =0	_	_	0.1	μΑ
$I_{\mathrm{EBO}}$	Emitter cut off current	$V_{EB}=5V$ , $I_{C}=0$	375	500	725	μΑ
hfe	DC forward current gain	$V_{CE}$ =5V, $I_{C}$ =5mA	30	_	_	_
VCE(sat)	Collector to Emitter saturation voltage	I <sub>C</sub> =10mA, I <sub>B</sub> =0.5mA	_	_	0.3	V
$R_2$	Emitter to Base resistor	_	7	10	13	kΩ
fT	Gain band width product	V <sub>CE</sub> =6V, I <sub>E</sub> =-10mA	_	200	_	MHz

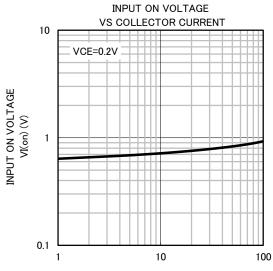
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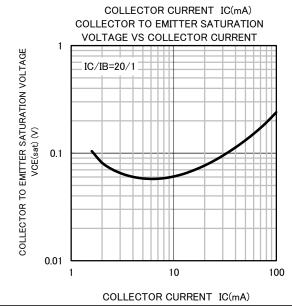
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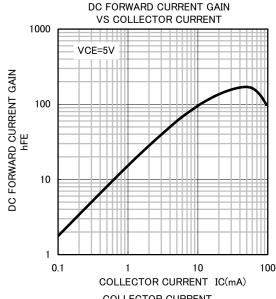
## TYPICAL CHARACTERISTICS

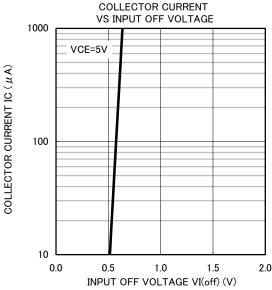
(Ta=25°C)(RTr1,RTr2 COMMON)











#### Keep safety first in your circuit designs!

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