RT3NPPM

Composite Transistor With Resistor For Switching Application Silicon Epitaxial Type

DESCRIPTION

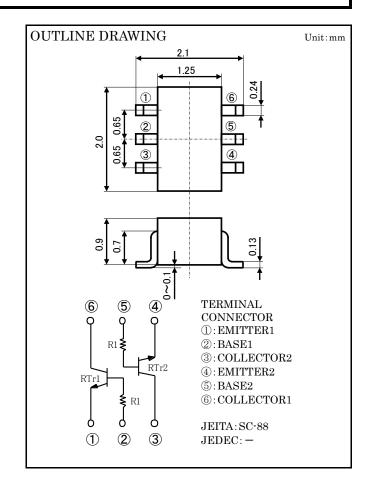
RT3NPPM is composite transistor built with two RT1N130 chips in SC-88 package.

FEATURE

Built-in bias resistor (R1=1 $k\Omega$) Mini package for easy mounting

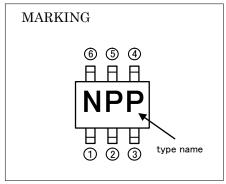
APPLICATION

Inverted circuit, Switching circuit, Interface circuit, Driver circuit



MAXIMUM RATING(Ta=25°C)(RTr1, RTr2 COMMON)

SYMBOL	PARAMETER	RATING	UNIT
Vcbo	Collector to Base voltage	50	V
V_{EBO}	Emitter to Base voltage	6	V
V_{CEO}	Collector to Emitter voltage	50	V
Ic	Collector current	100	mA
Icm	Peak Collector current	200	mA
PT	Total dissipation	200	mW
$T_{\rm j}$	Junction temperature	+150	ပ္
T_{stg}	Storage temperature	-55~+150	လူ



ELECTRICAL CHARACTERISTICS(Ta=25°C)(RTr1, RTr2 COMMON)

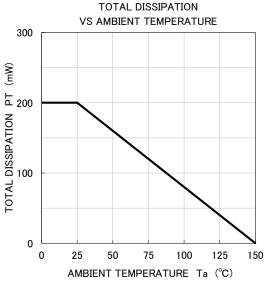
SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			TINIM
			MIN	TYP	MAX	UNIT
V(BR)CEO	Collector to Emitter breakdown voltage	$I_C=100 \mu$ A, $R_{BE}=\infty$	50	_	_	V
Icbo	Collector cut off current	$V_{CB} = 50V, I_{E} = 0$	_	_	0.1	μΑ
IEBO	Emitter cut off current	$V_{EB}=5V$, $I_C=0$	_	_	0.1	μΑ
hfe	DC forward current gain	$V_{CE}=5V$, $I_{C}=1mA$	100	_	_	_
VCE(sat)	Collector to Emitter saturation voltage	$I_C=10$ mA, $I_B=0.5$ mA	_	_	0.3	V
R_1	Input resistor	_	0.7	1.0	1.3	kΩ
$ m f_T$	Gain band width product	V _{CE} =6V, I _E =-10mA	_	200	_	MHz

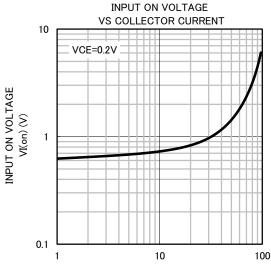
RT3NPPM

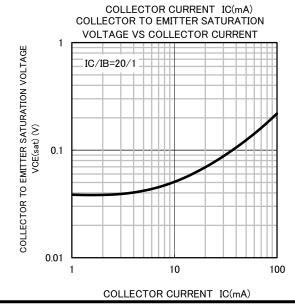
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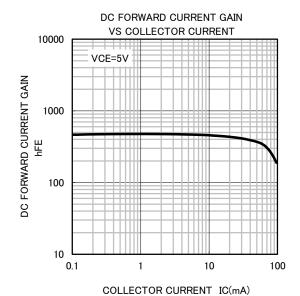
TYPICAL CHARACTERISTICS

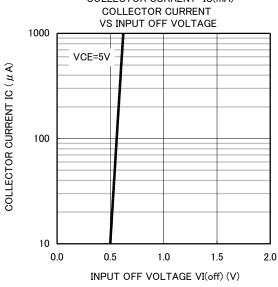
(Ta=25°C)(RTr1,RTr2 COMMON)











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