RT3NKKM

Composite Transistor With Resistor For Switching Application Silicon Epitaxial Type

DESCRIPTION

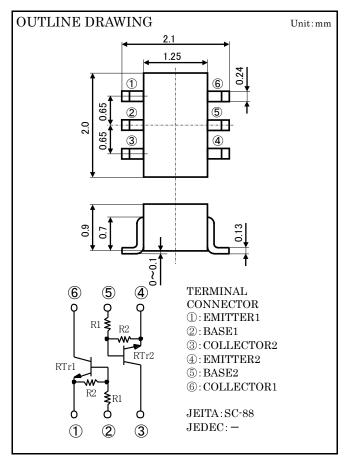
RT3NKKM is composite transistor built with two RT1N242 chips in SC-88 package.

FEATURE

Built-in bias resistor (R1= $22k\Omega$, R2= $47k\Omega$) Mini package for easy mounting

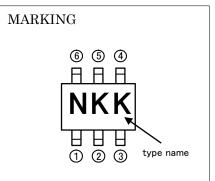
APPLICATION

Inverted circuit, Switching circuit, Interface circuit, Driver circuit



MAXIMUM RATING(Ta=25°C)(RTr1, RTr2 COMMON)

| SYMBOL | PARAMETER | RATING | UNIT |
|---------------|------------------------------|----------|------|
| VCBO | Collector to Base voltage | 50 | V |
| VEBO | Emitter to Base voltage | 10 | V |
| VCEO | Collector to Emitter voltage | 50 | V |
| $V_{\rm IN}$ | Input voltage | 40 | V |
| Ic | Collector current | 100 | mA |
| ICM | Peak Collector current | 200 | mA |
| Рт | Total dissipation | 200 | mW |
| Tj | Junction temperature | +150 | °C |
| $T_{\rm stg}$ | Storage temperature | -55~+150 | °C |



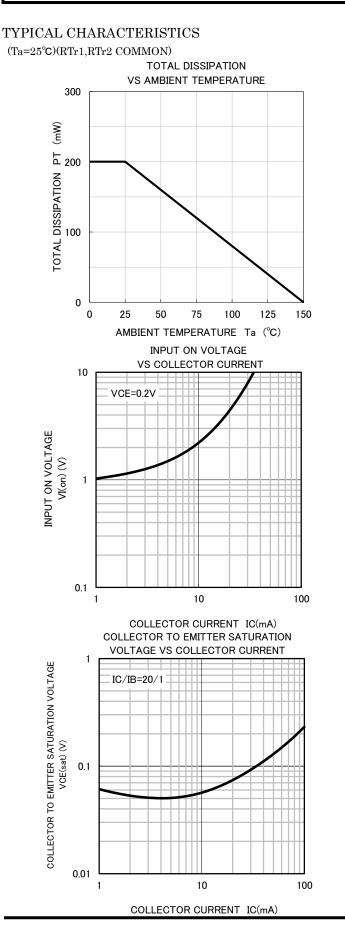
ELECTRICAL CHARACTERISTICS(Ta=25°C)(RTr1, RTr2 COMMON)

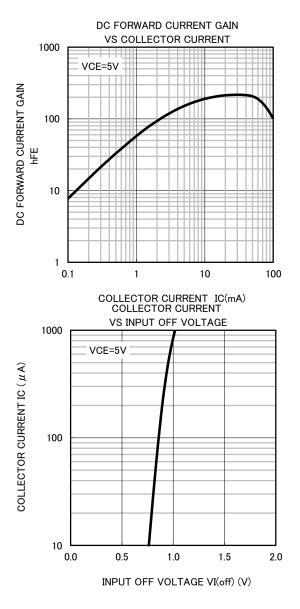
| SYMBOL | PARAMETER | TEST CONDITIONS | LIMITS | | | UNIT |
|---------------------|---|---|--------|-----|-----|------|
| | | | MIN | TYP | MAX | UNII |
| V(BR)CEO | Collector to Emitter breakdown voltage | $I_{C}=100 \mu$ A, $R_{BE}=\infty$ | 50 | — | — | V |
| Ісво | Collector cut off current | V_{CB} =50V, I_E =0 | _ | _ | 0.1 | μA |
| IEBO | Emitter cut off current | V _{EB} =5V, I _C =0 | 54 | 72 | 105 | μA |
| hFE | DC forward current gain | V _{CE} =5V, I _C =5mA | 68 | — | — | - |
| VCE(sat) | Collector to Emitter saturation voltage | I _C =10mA, I _B =0.5mA | — | — | 0.3 | V |
| V _{I(ON)} | Input on voltage | V _{CE} =0.2V, I _C =5mA | — | 1.2 | 3.0 | V |
| V _{I(OFF)} | Input off voltage | $V_{CE}=5V$, $I_C=100 \mu A$ | 0.6 | 0.8 | — | V |
| R_1 | Input resistor | - | 15 | 22 | 29 | kΩ |
| R_2/R_1 | Resistor ratio | - | 1.7 | 2.1 | 2.6 | _ |
| fT | Gain band width product | V _{CE} =6V, I _E =-10mA | — | 200 | — | MHz |

ISAHAYA ELECTRONICS CORPORATION

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