# RT3N33M-T150

Composite Transistor With Resistor For Switching Application Silicon Epitaxial Type

AEC-Q101 Compliance

#### DESCRIPTION

RT3N33M is composite transistor built with two RT1N441 chips in SC-88 package.

### **FEATURE**

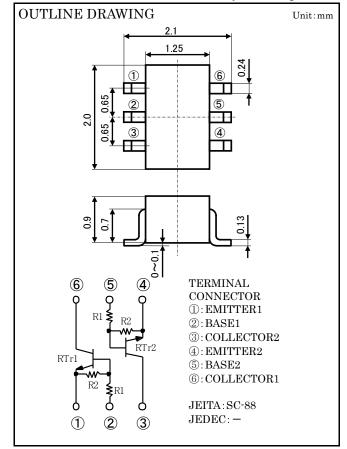
Silicon epitaxial type

Each transistor elements are independent.

Mini package for easy mounting

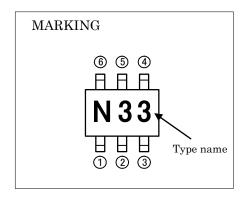
### APPLICATION

Inverted circuit, Switching circuit, Interface circuit, Driver circuit



## MAXIMUM RATING(Ta=25°C)(RTr1, RTr2 COMMON)

SYMBOL	PARAMETER	RATING	UNIT
Vcbo	Collector to Base voltage	50	V
$V_{\mathrm{EBO}}$	Emitter to Base voltage	10	V
$V_{CEO}$	Collector to Emitter voltage	50	V
$V_{\mathrm{IN}}$	Input voltage	40	V
$I_{\mathrm{C}}$	Collector current	100	mA
ICM	Peak Collector current	200	mA
PT	Total dissipation	200	mW
Tj	Junction temperature	+150	°C
$T_{\mathrm{stg}}$	Storage temperature	-55~+150	°C



### ELECTRICAL CHARACTERISTICS(Ta=25°C)(RTr1, RTr2 COMMON)

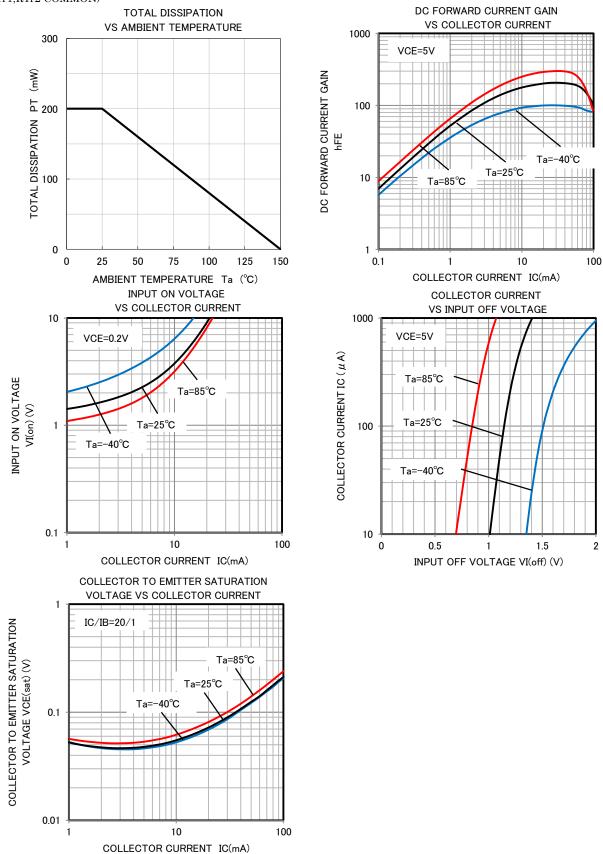
SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	UNII
V(BR)CEO	Collector to Emitter break down voltage	I <sub>C</sub> =100μA, R <sub>BE</sub> =∞	50	_	_	V
Icbo	Collector cut off current	$V_{CB}$ =50V, I $_{E}$ =0	_	_	0.1	μA
IEBO	Emitter cut off current	$V_{\rm EB}$ =5V, I $_{\rm C}$ =0	41	53	76	μA
$_{ m hFE}$	DC forward current gain	$V_{CE}$ =5 $V$ , I $_{C}$ =5 $mA$	50	_	_	_
VCE(sat)	Collector to Emitter saturation voltage	I $_{\rm C}$ =10mA, I $_{\rm B}$ =0.5mA	_	_	0.3	V
$V_{\rm I(ON)}$	Input on voltage	$V_{CE}$ =0.2 $V$ , $I_{C}$ =5 $mA$	_	2.2	5.0	V
$V_{\rm I(OFF)}$	Input off voltage	$V_{\rm CE}$ =5 $V$ , I $_{\rm C}$ =100 $\mu A$	0.8	1.1	_	V
$R_1$	Input resistor	_	33	47	61	kΩ
$R_2/R_1$	Resistor ratio	_	0.9	1.0	1.1	_
fT	Gain band width product	V <sub>CE</sub> =6V, I <sub>E</sub> =-10mA	_	200	_	MHz

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Composite Transistor With Resistor For Switching Application Silicon Epitaxial Type

# TYPICAL CHARACTERISTICS

(RTr1,RTr2 COMMON)



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