RT2P20M

Composite Transistor With Resistor For Switching Application Silicon PNP Epitaxial Type

DESCRIPTION

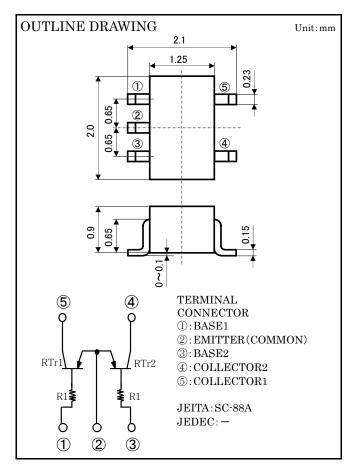
RT2P20M is composite transistor with built-in bias resistor.

FEATURE

Built-in bias resistor (R1= $4.7k\Omega$) Mini package for easy mounting

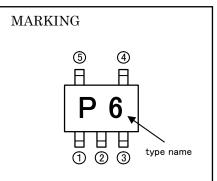
APPLICATION

Inverted circuit, Switching circuit, Interface circuit, Driver circuit



MAXIMUM RATING(Ta=25°C)(RTr1, RTr2 COMMON)

SYMBOL	PARAMETER	RATING	UNIT	
VCBO	Collector to Base voltage	-50	V	
Vebo	Emitter to Base voltage	-6	V	
VCEO	Collector to Emitter voltage	-50	V	
IC	Collector current	-100	mA	
ICM	Peak Collector current	-200	mA	
\mathbf{P}_{T}	Total dissipation	200	mW	
Tj	Junction temperature	+150	°C	
T_{stg}	Storage temperature	-55~+150	°C	

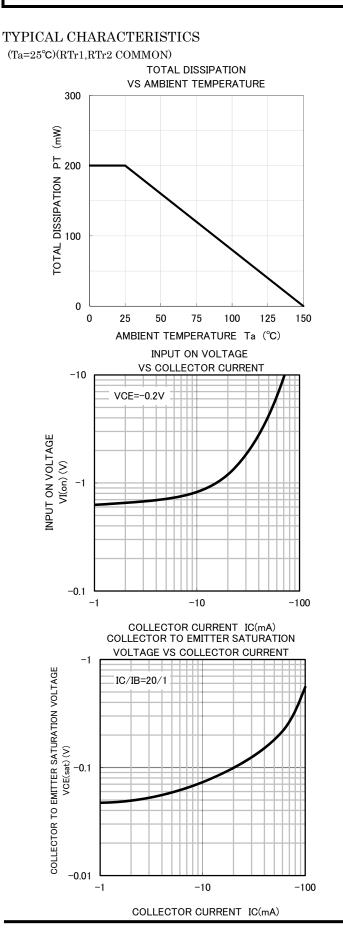


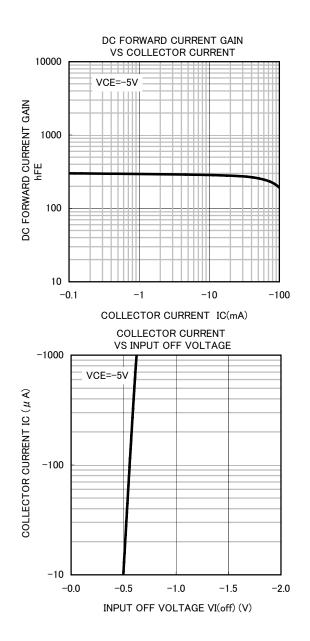
ELECTRICAL CHARACTERISTICS(Ta=25°C)(RTr1, RTr2 COMMON)

SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	UNII
V(BR)CEO	Collector to Emitter breakdown voltage	$I_C=-100 \mu$ A, $R_{BE}=\infty$	-50	—	—	V
ICBO	Collector cut off current	V_{CB} =-50V, I _E =0	Ι	—	-0.1	μA
IEBO	Emitter cut off current	V_{EB} =-5V, I_C =0	1	—	-0.1	μA
hfe	DC forward current gain	V_{CE} =-5V, Ic=-1mA	100	—	_	_
VCE(sat)	Collector to Emitter saturation voltage	Ic=-10mA, I _B =-0.5mA	Ι	-0.1	-0.3	V
R_1	Input resistor	-	3.3	4.7	6.1	kΩ
f_{T}	Gain band width product	V_{CE} =-6V, I _E =10mA		150	_	MHz

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