## RT2P05M

Composite Transistor With Resistor For Switching Application Silicon PNP Epitaxial Type

#### DESCRIPTION

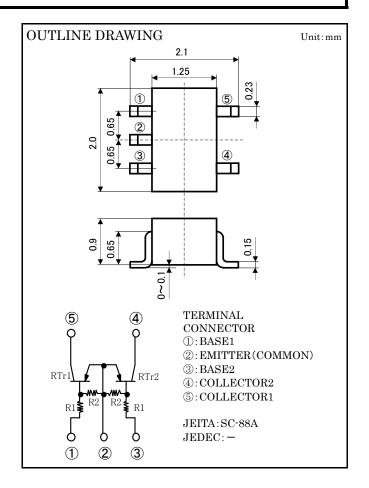
RT2P05M is composite transistor with built-in bias resistor.

#### **FEATURE**

Built-in bias resistor (R1=47k $\Omega$ , R2=47k $\Omega$ ) Mini package for easy mounting

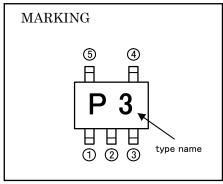
#### APPLICATION

Inverted circuit, Switching circuit, Interface circuit, Driver circuit



## MAXIMUM RATING(Ta=25°C)(RTr1, RTr2 COMMON)

SYMBOL	PARAMETER	RATING	UNIT
Vcbo	Collector to Base voltage	-50	V
VEBO	Emitter to Base voltage	-10	V
VCEO	Collector to Emitter voltage	-50	V
$V_{\rm IN}$	Input voltage	-40	V
Ic	Collector current	-100	mA
Icm	Peak Collector current	-200	mA
PT	Total dissipation	200	mW
Tj	Junction temperature	+150	°C
$T_{ m stg}$	Storage temperature	-55~+150	လ



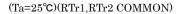
## ELECTRICAL CHARACTERISTICS(Ta=25°C)(RTr1, RTr2 COMMON)

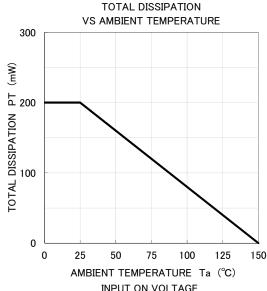
SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			TINITO
			MIN	TYP	MAX	UNIT
V(BR)CEO	Collector to Emitter breakdown voltage	$I_C=-100 \mu$ A, $R_{BE}=\infty$	-50	_	_	V
Icbo	Collector cut off current	V <sub>CB</sub> =-50V, I <sub>E</sub> =0	_	_	-0.1	μΑ
IEBO	Emitter cut off current	$V_{EB}$ =-5V, $I_C$ =0	-41	-53	-76	μΑ
hfe	DC forward current gain	$V_{CE}$ =-5 $V$ , $I_{C}$ =-5 $mA$	50	_	_	_
VCE(sat)	Collector to Emitter saturation voltage	I <sub>C</sub> =-10mA, I <sub>B</sub> =-0.5mA	_	-0.1	-0.3	V
$V_{\rm I(ON)}$	Input on voltage	V <sub>CE</sub> =-0.2V, I <sub>C</sub> =-5mA	_	-2.2	-5.0	V
$V_{\rm I(OFF)}$	Input off voltage	$V_{\rm CE}$ =-5V, $I_{\rm C}$ =-100 $\mu$ A	-0.8	-1.1	_	V
R <sub>1</sub>	Input resistor	_	33	47	61	kΩ
$R_2/R_1$	Resistor ratio	_	0.9	1.0	1.1	_
$f_{\mathrm{T}}$	Gain band width product	V <sub>CE</sub> =-6V, I <sub>E</sub> =10mA	_	150	_	MHz

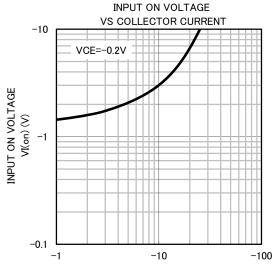
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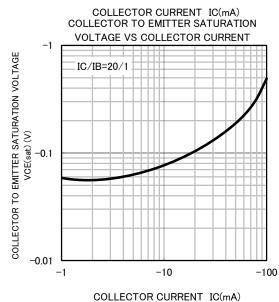
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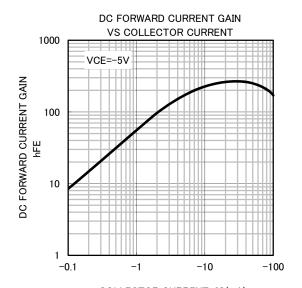
#### TYPICAL CHARACTERISTICS

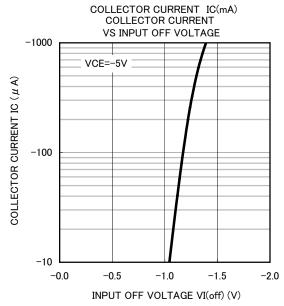














#### Keep safety first in your circuit designs!

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