RT2P01M

Composite Transistor With Resistor For Switching Application Silicon PNP Epitaxial Type

DESCRIPTION

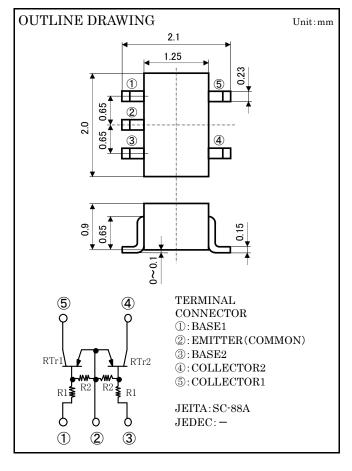
RT2P01M is composite transistor with built-in bias resistor.

FEATURE

Built-in bias resistor (R1=2.2k Ω , R2=2.2k Ω) Mini package for easy mounting

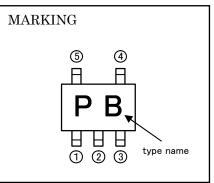
APPLICATION

Inverted circuit, Switching circuit, Interface circuit, Driver circuit



MAXIMUM RATING(Ta=25°C)(RTr1, RTr2 COMMON)

SYMBOL	PARAMETER	RATING	UNIT
VCBO	Collector to Base voltage	-50	V
Vebo	Emitter to Base voltage	-10	V
VCEO	Collector to Emitter voltage	-50	V
$V_{\rm IN}$	Input voltage	-12	V
IC	Collector current	-100	mA
ICM	Peak Collector current	-200	mA
Рт	Total dissipation	200	mW
Tj	Junction temperature	+150	°C
$T_{\rm stg}$	Storage temperature	-55~+150	°C



ELECTRICAL CHARACTERISTICS(Ta=25°C)(RTr1, RTr2 COMMON)

SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			
			MIN	TYP	MAX	UNIT
V(BR)CEO	Collector to Emitter breakdown voltage	I_{C} =-100 μ A, R_{BE} = ∞	-50	_	-	V
ICBO	Collector cut off current	V _{CB} =-50V, I _E =0	—	_	-0.1	μA
IEBO	Emitter cut off current	V_{EB} =-5V, I_C =0	-850	-1140	-1650	μA
hFE	DC forward current gain	V _{CE} =-5V, I _C =-20mA	20	—	—	-
VCE(sat)	Collector to Emitter saturation voltage	Ic=-10mA, I _B =-0.5mA	—	_	-0.3	V
V _{I(ON)}	Input on voltage	V _{CE} =-0.2V, I _C =-5mA	—	-1.3	-2.2	V
V _{I(OFF)}	Input off voltage	V_{CE} =-5V, I _C =-100 μ A	-0.7	-1.1	-	V
R1	Input resistor	-	1.5	2.2	2.9	kΩ
R_2/R_1	Resistor ratio	_	0.8	1.0	1.2	_
f_{T}	Gain band width product	V_{CE} =-6V, I_E =10mA	_	150	_	MHz

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RT2P01M

Composite Transistor With Resistor For Switching Application Silicon PNP Epitaxial Type

Ta=-40°C

-100

Ta=25°C

-10

DC FORWARD CURRENT GAIN

Ta=85°C

COLLECTOR CURRENT

VS INPUT OFF VOLTAGE

COLLECTOR CURRENT IC(mA)

-1

Ta=85°C

Ta=25°C

Ta=-40°C

-0.4

-0.8

-1.2

INPUT OFF VOLTAGE VI(off) (V)

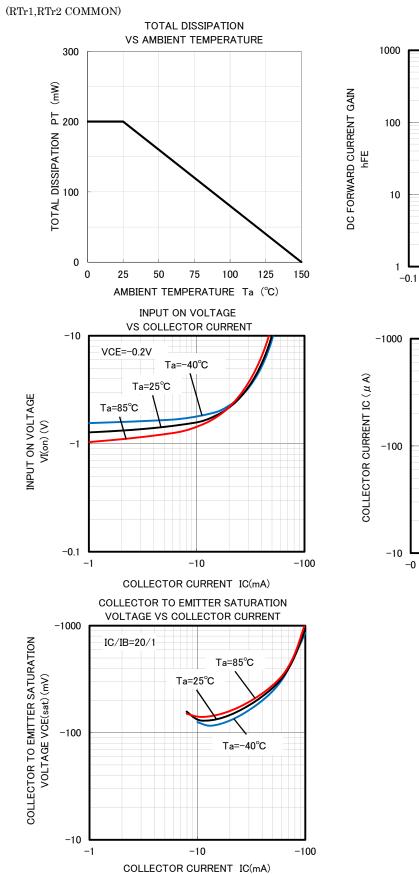
-1.6

-2

VCE=-5V

VCE=-5V

VS COLLECTOR CURRENT



TYPICAL CHARACTERISTICS

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