# RT2N11M

Composite Transistor With Resistor For Switching Application Silicon NPN Epitaxial Type

### DESCRIPTION

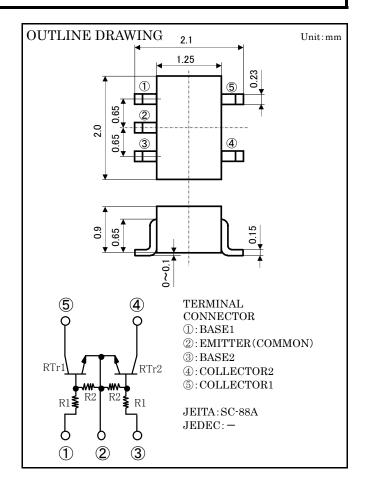
RT2N11M is composite transistor with built-in bias resistor.

### **FEATURE**

Built-in bias resistor (R1=4.7k $\Omega$ , R2=22k $\Omega$ ) Mini package for easy mounting

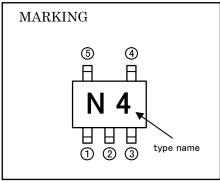
### **APPLICATION**

Inverted circuit, Switching circuit, Interface circuit, Driver circuit



## MAXIMUM RATING(Ta=25°C)(RTr1, RTr2 COMMON)

SYMBOL	PARAMETER	RATING	UNIT	
Vcbo	Collector to Base voltage	50	V	
VEBO	Emitter to Base voltage	6	V	
$V_{CEO}$	Collector to Emitter voltage	50	V	
$V_{\mathrm{IN}}$	Input voltage	30	V	
Ic	Collector current	100	mA	
Icm	Peak Collector current	200	mA	
PT	Total dissipation	200	mW	
Tj	Junction temperature	+150	°C	
$T_{ m stg}$	Storage temperature	-55~+150	°C	



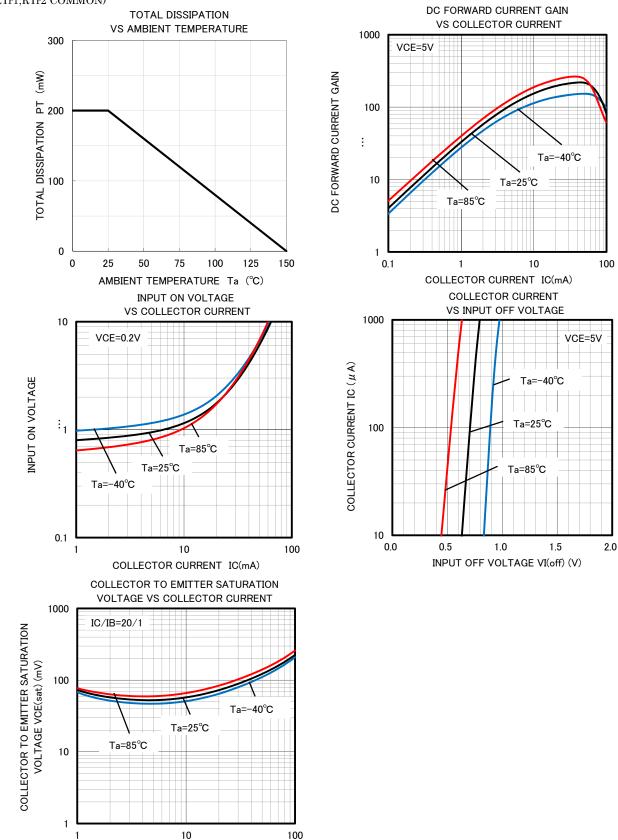
## ELECTRICAL CHARACTERISTICS(Ta=25°C)(RTr1, RTr2 COMMON)

SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			TINIIM
			MIN	TYP	MAX	UNIT
V(BR)CEO	Collector to Emitter breakdown voltage	I <sub>C</sub> =100 <i>μ</i> A, R <sub>BE</sub> =∞	50	_	_	V
$I_{CBO}$	Collector cut off current	$V_{CB}$ =50V, $I_E$ =0	_	_	0.1	μΑ
IEBO	Emitter cut off current	$V_{EB}$ =5V, $I_C$ =0	147	187	259	μΑ
$_{ m hFE}$	DC forward current gain	V <sub>CE</sub> =5V, I <sub>C</sub> =5mA	50	_	_	_
VCE(sat)	Collector to Emitter saturation voltage	$I_C=10$ mA, $I_B=0.5$ mA	_	0.1	0.3	V
$V_{\rm I(ON)}$	Input on voltage	$V_{CE}$ =0.2 $V$ , $I_{C}$ =5 $mA$	_	0.9	1.7	V
$V_{\rm I(OFF)}$	Input off voltage	$V_{\rm CE}$ =5 $V$ , $I_{\rm C}$ =100 $\mu$ A	0.5	0.7	_	V
$R_1$	Input resistor	_	3.3	4.7	6.1	kΩ
$R_2/R_1$	Resistor ratio	_	4.2	4.7	5.1	_
$ m f_{T}$	Gain band width product	V <sub>CE</sub> =6V, I <sub>E</sub> =-10mA	_	200	_	MHz

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COLLECTOR CURRENT IC(mA)



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