# RT2N09M

Composite Transistor With Resistor For Switching Application Silicon NPN Epitaxial Type

#### DESCRIPTION

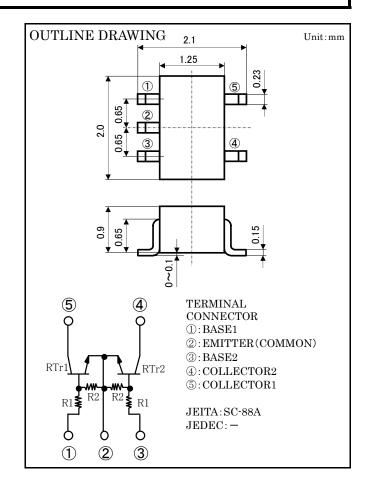
RT2N09M is composite transistor with built-in bias resistor.

### **FEATURE**

Built-in bias resistor (R1= $2.2k\Omega$ , R2= $47k\Omega$ ) Mini package for easy mounting

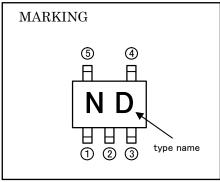
### APPLICATION

Inverted circuit, Switching circuit, Interface circuit, Driver circuit



### MAXIMUM RATING(Ta=25°C)(RTr1, RTr2 COMMON)

SYMBOL	PARAMETER	RATING	UNIT	
Vcbo	Collector to Base voltage	50	V	
VEBO	Emitter to Base voltage	6	V	
$V_{CEO}$	Collector to Emitter voltage	50	V	
$V_{\mathrm{IN}}$	Input voltage	12	V	
Ic	Collector current	100	mA	
Icm	Peak Collector current	200	mA	
PT	Total dissipation	200	mW	
Tj	Junction temperature	+150	°C	
$T_{ m stg}$	Storage temperature	-55~+150	°C	



## $ELECTRICAL\ CHARACTERISTICS (Ta=25°C) (RTr1,\ RTr2\ COMMON)$

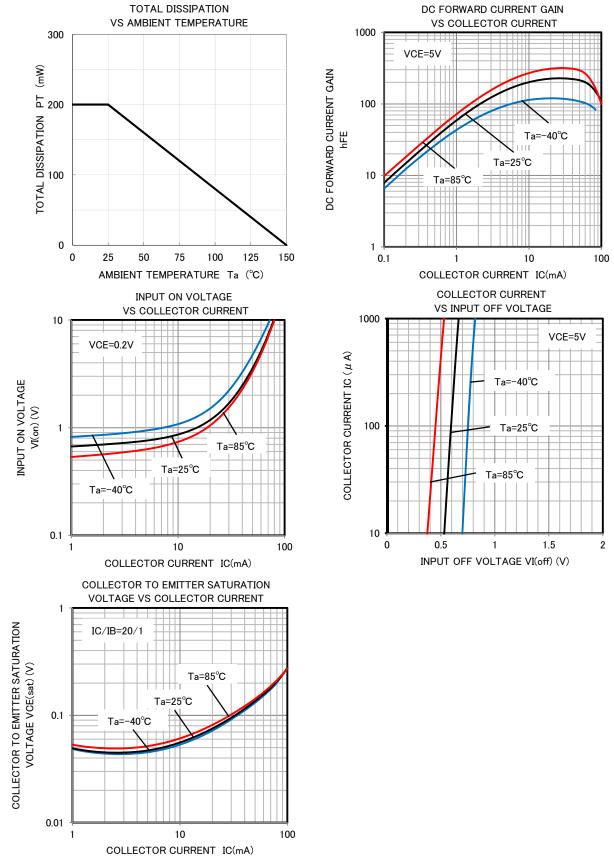
SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			TINITE
			MIN	TYP	MAX	UNIT
V(BR)CEO	Collector to Emitter breakdown voltage	I <sub>C</sub> =100 μ A, R <sub>BE</sub> =∞	50	_	_	V
Icbo	Collector cut off current	V <sub>CB</sub> =50V, I <sub>E</sub> =0	_	_	0.1	μΑ
IEBO	Emitter cut off current	$V_{EB}$ =5V, $I_C$ =0	76	102	147	μΑ
$_{ m hFE}$	DC forward current gain	V <sub>CE</sub> =5V, I <sub>C</sub> =10mA	80	_	_	_
VCE(sat)	Collector to Emitter saturation voltage	$I_C=10$ mA, $I_B=0.5$ mA	_	_	0.3	V
$V_{\rm I(ON)}$	Input on voltage	V <sub>CE</sub> =0.2V, I <sub>C</sub> =5mA	_	0.7	1.1	V
$V_{\rm I(OFF)}$	Input off voltage	$V_{\rm CE} = 5V$ , $I_{\rm C} = 100 \mu$ A	0.5	0.6	_	V
R <sub>1</sub>	Input resistor	_	1.5	2.2	2.9	kΩ
$R_2/R_1$	Resistor ratio	_	17	22	26	_
$ m f_T$	Gain band width product	V <sub>CE</sub> =6V, I <sub>E</sub> =-10mA	_	200	_	MHz

# RT2N09M

Composite Transistor With Resistor For Switching Application Silicon NPN Epitaxial Type

# TYPICAL CHARACTERISTICS

(RTr1,RTr2 COMMON)





#### Keep safety first in your circuit designs!

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