Composite Transistor For Muting Application

### **DESCRIPTION**

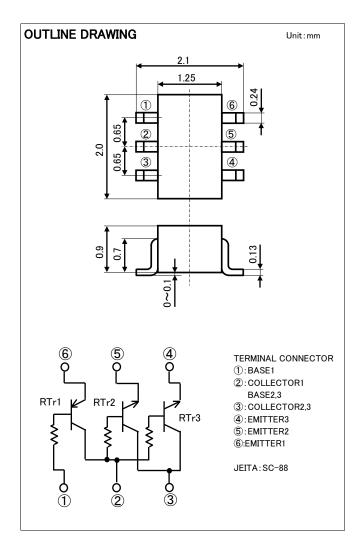
RT3YA7M is a composite transistor built with RT1P140 and two muting transistor with resistor in SC-88 package.

### **FEATURE**

- •RT3YA7M is built in RTr1 side RT1P140,and RTr2,RTr3 side composite muting transistor with resistor.
- \*Built-in bias resistor  $\mbox{RTr1:R}_i \mbox{=} 10 \mbox{k} \ \mbox{R} \ \mbox{RTr2,RTr3:R}_i \mbox{=} 4.7 \mbox{k} \ \mbox{\Omega}$
- •Mini package for easy mounting

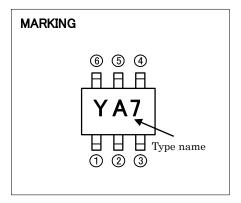
#### **APPLICATION**

muting circuit, switching circuit



## MAXIMUM RATING (Ta=25°C)

				,	
SYMBOL	PARAMETER	RTr1	RTr2,RTr3	UNIT	
		RATING	RATING		
$V_{\text{CBO}}$	Collector to Base voltage	-9	40	V	
$V_{EBO}$	Emitter to Base voltage	-50	40	٧	
$V_{\text{CEO}}$	Collector to Emitter voltage	-9	15	V	
$\mathbf{I}_{C}$	Collector current	-100	200	mA	
$P_{T}$	Total dissipation	150		mW	
$T_{j}$	Junction temperature	+150		°C	
$T_{stg}$	Storage temperature	−55 <b>~</b> +150		°C	



### RT3YA7M

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## ELECTRICAL CHARACTERISTICS (Ta=25°C) (RTr1 side)

Symbol	Parameter	Test conditions	Limits			Unit
			Min	Тур	Max	Unit
$V_{\text{CBO}}$	Collector-base breakdown voltage	$I_C = -50\mu A$ , $I_E = 0mA$	-9	_	ı	V
$V_{EBO}$	Emitter-base breakdown voltage	$I_E=-50\mu A$ , $I_C=0mA$	-50	_	-	V
$V_{\text{CEO}}$	Collector-emitter breakdown voltage	$I_C=-1$ mA, $R_{BE}=\infty$	-9	_	ı	V
$I_{CBO}$	Collector cutoff current	V <sub>CB</sub> =-6V, I <sub>E</sub> =0mA	-	_	-0.1	μA
$\mathbf{I}_{EBO}$	Emitter cutoff current	$V_{EB}$ =-50V, $I_{C}$ =0mA	-	_	-0.1	μA
h <sub>FE</sub>	DC current transfer ratio	V <sub>CE</sub> =-5V, I <sub>C</sub> =-1mA	-	10	_	_
R <sub>1</sub>	Input resistance	-	-	10	_	kΩ

# ELECTRICAL CHARACTERISTICS (Ta=25°C) (RTr2,RTr3 common)

Symbol	Parameter	Test conditions	Limits			11.2
			Min	Тур	Max	Unit
$V_{\text{CBO}}$	Collector-base breakdown voltage	$I_C=50\mu A$ , $I_E=0mA$	40	-	ı	<b>\</b>
$V_{EBO}$	Emitter-base breakdown voltage	I <sub>E</sub> =50μA, I <sub>C</sub> =0mA	40	-	ı	<b>\</b>
$V_{\text{CEO}}$	Collector-emitter breakdown voltage	I <sub>C</sub> =1 mA, R <sub>BE</sub> =∞	15	-	ı	<b>\</b>
$I_{CBO}$	Collector cutoff current	V <sub>CB</sub> =40V, I <sub>E</sub> =0mA	1	-	0.5	μΑ
$\mathbf{I}_{EBO}$	Emitter cutoff current	V <sub>EB</sub> =40V, I <sub>C</sub> =0mA	1	-	0.5	μΑ
h <sub>FE</sub>	DC current transfer ratio	$V_{CE}$ =5V, $I_{C}$ =10mA	820	-	2500	1
$V_{\text{CE(sat)}}$	Collector-emitter saturation voltage	I <sub>C</sub> =50mA, I <sub>B</sub> =5mA	1	-	100	mV
$R_1$	Input resistance	-	1	4.7	1	kΩ
f <sub>T</sub>	Transition frequency	V <sub>CE</sub> =6V, I <sub>E</sub> =-10mA	_	58	-	MHz
Ron	Output On-resistance	V <sub>IN</sub> =3V, f=1MHz	_	1.8	_	Ω

#### Keep safety first in your circuit designs!

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