Composite Transistor For high speed switching Silicon P-channel MOSFET

DESCRIPTION

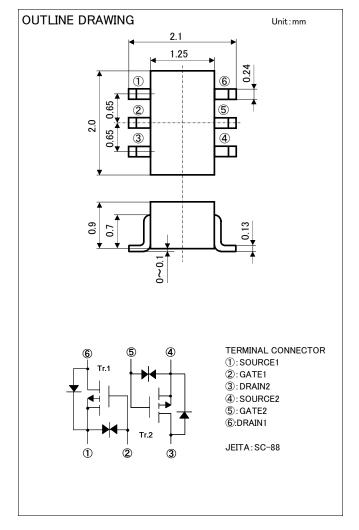
RT3J33M is a composite transistor built with two INJ0003AX chips in SC-88 package.

FEATURE

- •Input impedance is high, and not necessary to consider a drive electric current.
- •Drive voltage is -2.5V
- •Low on Resistance. RON=2 Ω (TYP)
- · High speed switching.
- ·Small package for easy mounting.

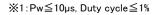
APPLICATION

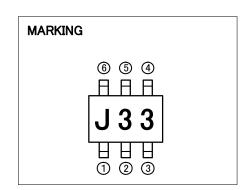
High speed switching, Analog switching



MAXIMUM RATING (Ta=25°C) (Tr1,Tr2 Common)

PARAMETER	RATING	UNIT						
Drain-source voltage	-20	٧						
Gate-source voltage	±8	٧						
Drain current(DC)	-200	mA						
Drain current(Pulse)	-400(※1)	mA						
Total power dissipation	150	mW						
Channel temperature	+150	္င						
Range of Storage temperature	-55 ~ +150	°C						
	PARAMETER Drain-source voltage Gate-source voltage Drain current(DC) Drain current(Pulse) Total power dissipation Channel temperature	PARAMETER RATING Drain−source voltage −20 Gate−source voltage ±8 Drain current(DC) −200 Drain current(Pulse) −400(※1) Total power dissipation 150 Channel temperature +150						



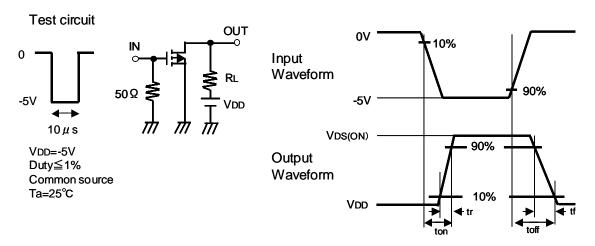


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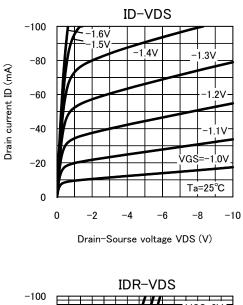
ELECTRICAL CHARACTERISTICS (Ta=25°C) (Tr1,Tr2 Common)

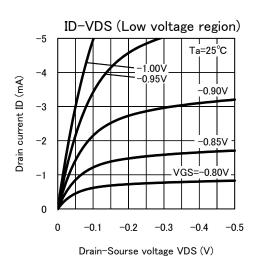
SYMBOL	PARAMETER	TEST CONDITION	LIMIT			UNIT
			MIN	TYP	MAX	UNIT
V(BR)DSS	Drain-source breakdown voltage	ID=-100µA, VGS=0V	-20	_	_	٧
Igss	Gate-source leak current	Vgs=±5V, Vps=0V	_	_	±0.5	μΑ
I DSS	Zero gate voltage drain current	VDS=-20V, VGS=0V	_	_	-1.0	μΑ
V_{th}	Gate threshold voltage	ID=-250μA, VDS=VGS	-0.6	_	-1.2	V
Yfs	Forward transfer admittance	VDS=-10V, ID=-0.1A	_	280	_	mS
RDS(ON)	Static drain-source on-state resistance	ID=-100mA, VGS=-4.0V	_	2.0	_	Ω
Ciss	Input capacitance	VDS=-10V, VGS=0V, f=1MHz	-	37	_	
Coss	Output capacitance		_	12	_	pF
ton	Switching time	V _{DD} =-5V, I _D =-10mA	-	16	_	
toff		V _G S=0~-5V	_	110	-	ns

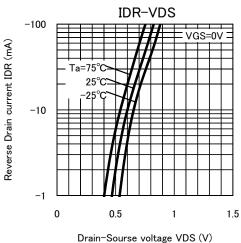
Switching time test condition

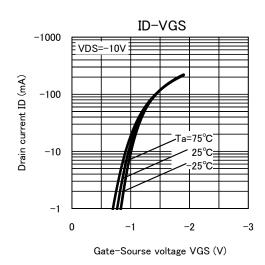


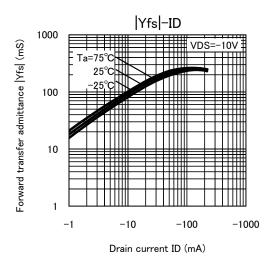
TYPICAL CHARACTERISTICS(Tr1,Tr2 Common)

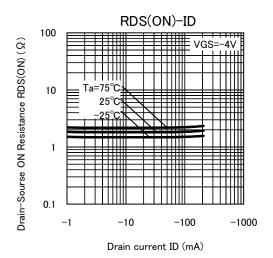




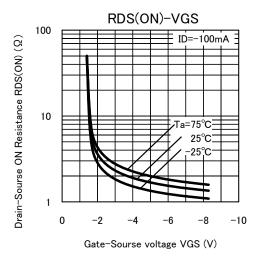


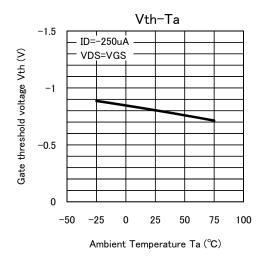


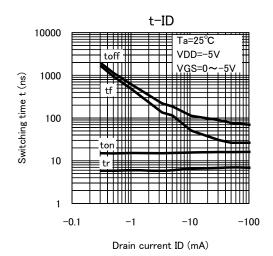


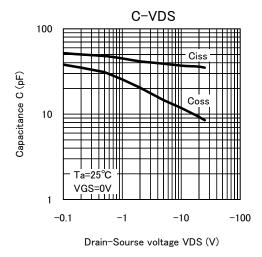


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