RT3T22M

Composite Transistor With Resistor For Switching Application Silicon Epitaxial Type

DESCRIPTION

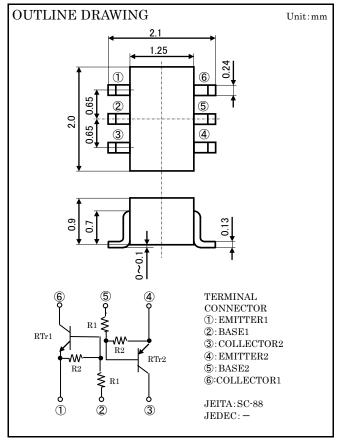
RT3T22M is composite transistor built with RT1N241 chip and RT1P241 chip in SC-88 package.

FEATURE

Built-in bias resistor (R1=22kΩ, R2=22kΩ) Mini package for easy mounting

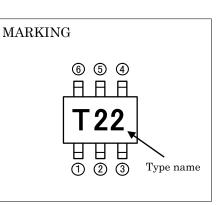
APPLICATION

Inverted circuit, Switching circuit, Interface circuit, Driver circuit



MAXIMUM RATING (Ta=25°C) (RTr1_NPN, RTr2_PNP)

SYMBOL	PARAMETER	RATING	UNIT	
VCBO	Collector to Base voltage	50	V	
Vebo	Emitter to Base voltage	10	V	
VCEO	Collector to Emitter voltage	50	V	
VIN	Input voltage	40	V	
IC	Collector current	100	mA	
ICM	Peak Collector current	200	mA	
\mathbf{P}_{T}	Total dissipation	200	mW	
Tj	Junction temperature	+150	°C	
T_{stg}	Storage temperature	-55~+150	°C	



※PNP built in transistor of "−"sign is abbreviation.

ELECTRICAL CHARACTERISTICS (Ta=25°C) (RTr1_NPN, RTr2_PNP)

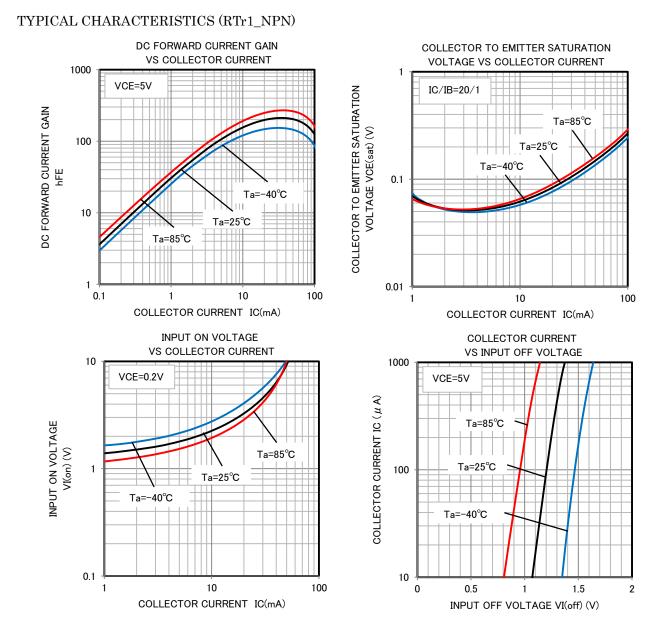
a	Parameter	Test conditions		Limits			TT 1.
Symbol				Min	Тур	Max	Unit
V(BR)CEO	Collector to Emitter breakdown voltage	$I_{C}=100 \mu$ A, $R_{BE}=\infty$		50	—	—	V
ICBO	Collector cut off current V_{CB} =50V, I_E =0		—	—	0.1	μA	
IEBO	Emitter cut off current	$V_{EB}=5V$, $I_C=0$		89	113	156	μA
hfe	DC forward current gain	V _{CE} =5V, I _C =5mA		50	—	—	-
VCE(sat)	Collector to Emitter saturation voltage Ic=10mA, IB=0.5mA		—	0.1	0.3	V	
VI(ON)	Input on voltage $V_{CE}=0.2V, I_C=5mA$		—	1.8	3.0	V	
VI(OFF)	Input off voltage $V_{CE}=5V$, $I_C=100 \mu$ A		0.8	1.1	—	V	
R_1	Input resistor	-		16	22	28	kΩ
R_2/R_1	Resistor ratio	-		0.9	1.0	1.1	—
\mathbf{f}_{T}	Gain band width product	V _{CE} =6V, I _E =10mA	RTr1	—	200	—	MHz
			RTr2	_	150	_	

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RT3T22M

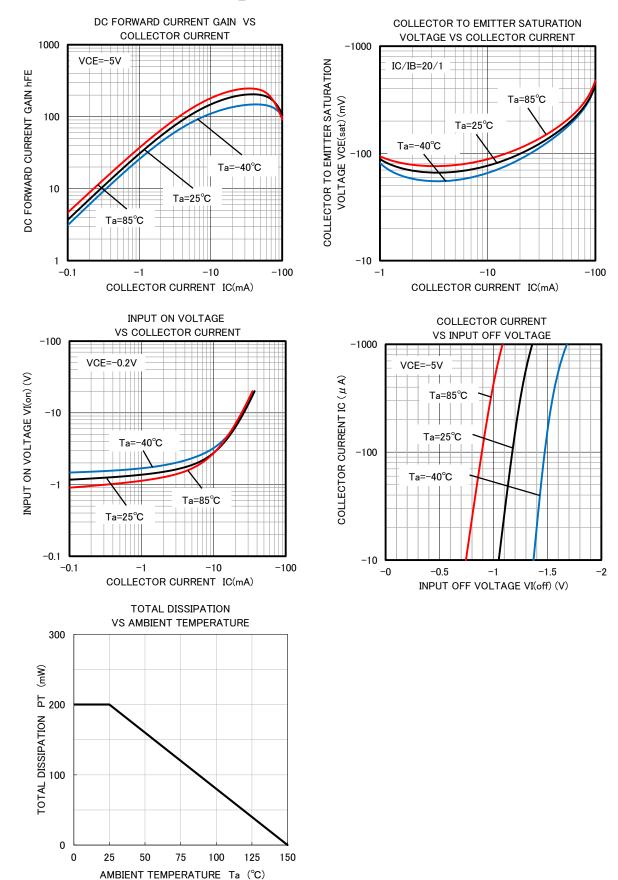
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TYPICAL CHARACTERISTICS (RTr 2_PNP)

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