RT3PDDM

Composite Transistor With Resistor For Switching Application Silicon Epitaxial Type

DESCRIPTION

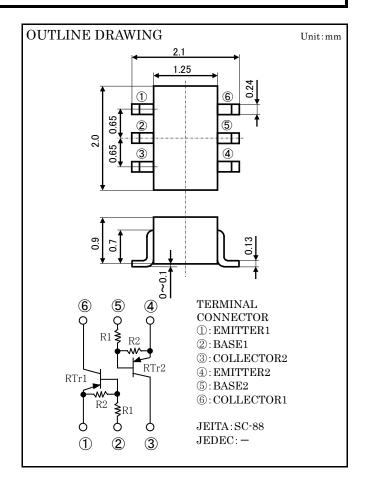
RT3PDDM is composite transistor built with two RT1P237 chips in SC-88 package.

FEATURE

Built-in bias resistor (R1=2.2k Ω , R2=47k Ω) Mini package for easy mounting

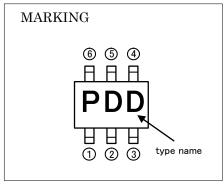
APPLICATION

Inverted circuit, Switching circuit, Interface circuit, Driver circuit



MAXIMUM RATING(Ta=25°C)(RTr1, RTr2 COMMON)

SYMBOL	PARAMETER	RATING	UNIT
Vcbo	Collector to Base voltage	-50	V
V_{EBO}	Emitter to Base voltage	-6	V
V_{CEO}	Collector to Emitter voltage	-50	V
$V_{\rm IN}$	Input voltage	-12	V
I_{C}	Collector current	-100	mA
Icm	Peak Collector current	-200	mA
P_{T}	Total dissipation	200	mW
Tj	Junction temperature	+150	°C
T_{stg}	Storage temperature	-55~+150	ပ္



$ELECTRICAL\ CHARACTERISTICS (Ta=25°C) (RTr1,\ RTr2\ COMMON)$

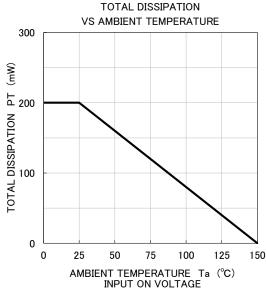
SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			TINIIM
			MIN	TYP	MAX	UNIT
V(BR)CEO	Collector to Emitter breakdown voltage	$I_C=-100 \mu$ A, $R_{BE}=\infty$	-50	_	_	V
ICBO	Collector cut off current	V_{CB} =-50V, I_{E} =0	_	_	-0.1	μΑ
I_{EBO}	Emitter cut off current	V_{EB} =-5 V , I_{C} =0	-76	-102	-147	μΑ
$_{ m hFE}$	DC forward current gain	V_{CE} =-5V, I_{C} =-10mA	80	_	_	_
VCE(sat)	Collector to Emitter saturation voltage	I_{C} =-10mA, I_{B} =-0.5mA	_	_	-0.3	V
$V_{\rm I(ON)}$	Input on voltage	V_{CE} =-0.2V, I_{C} =-5mA	_	-0.7	-1.1	V
$V_{\rm I(OFF)}$	Input off voltage	V_{CE} =-5 V , I_{C} =-100 μ A	-0.5	-0.6	_	V
R_1	Input resistor	_	1.5	2.2	2.9	kΩ
R_2/R_1	Resistor ratio	_	17	22	26	_
fT	Gain band width product	V _{CE} =-6V, I _E =10mA	_	150	_	MHz

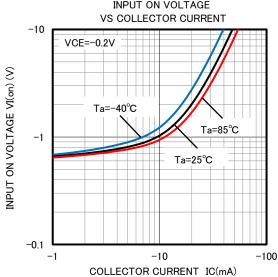
RT3PDDM

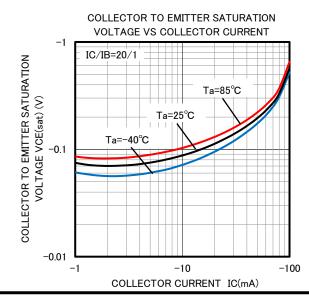
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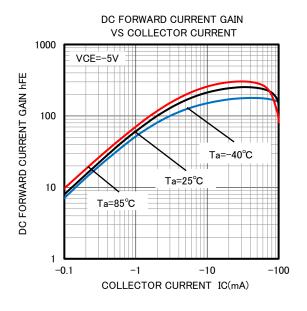
TYPICAL CHARACTERISTICS

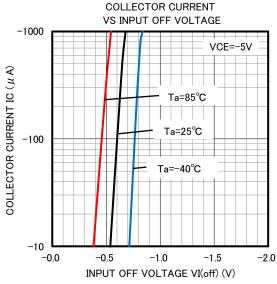
(RTr1,RTr2 COMMON)











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