RT3NQQM

Composite Transistor With Resistor For Switching Application Silicon Epitaxial Type

DESCRIPTION

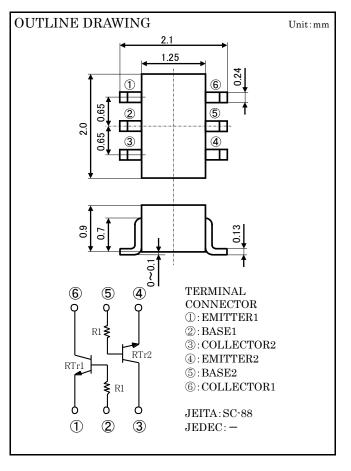
RT3NQQM is composite transistor built with two RT1N240 chips in SC-88 package.

FEATURE

Built-in bias resistor (R1= $22k\Omega$) Mini package for easy mounting

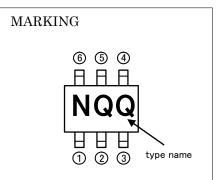
APPLICATION

Inverted circuit, Switching circuit, Interface circuit, Driver circuit



MAXIMUM RATING(Ta=25°C)(RTr1, RTr2 COMMON)

SYMBOL	PARAMETER	RATING	UNIT
VCBO	Collector to Base voltage	50	V
VEBO	Emitter to Base voltage	6	V
VCEO	Collector to Emitter voltage	50	V
IC	Collector current	100	mA
ICM	Peak Collector current	200	mA
Рт	Total dissipation	200	mW
Tj	Junction temperature	+150	°C
$T_{\rm stg}$	Storage temperature	-55~+150	°C

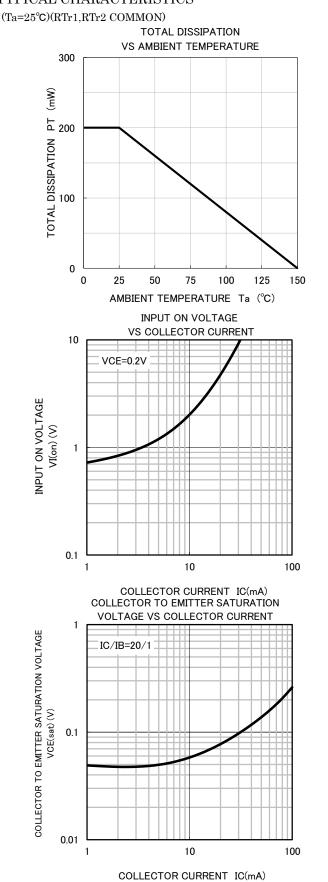


ELECTRICAL CHARACTERISTICS(Ta=25°C)(RTr1, RTr2 COMMON)

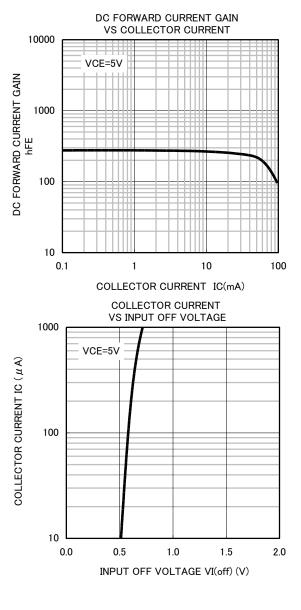
SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	UNII
V(BR)CEO	Collector to Emitter breakdown voltage	$I_C=100 \mu$ A, $R_{BE}=\infty$	50	—	_	V
Ісво	Collector cut off current	V_{CB} =50V, I_E =0	_	_	0.1	μA
IEBO	Emitter cut off current	$V_{EB}=5V$, $I_C=0$	_	_	0.1	μA
hFE	DC forward current gain	V _{CE} =5V, I _C =1mA	100	_	—	—
VCE(sat)	Collector to Emitter saturation voltage	$I_C=10mA$, $I_B=0.5mA$	_	_	0.3	V
R ₁	Input resistor	-	15	22	29	kΩ
$_{\rm fr}$	Gain band width product	$V_{CE}=6V$, $I_{E}=-10mA$	—	200	_	MHz

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TYPICAL CHARACTERISTICS



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