RT3N22M-T150

Composite Transistor With Resistor For Switching Application Silicon Epitaxial Type

AEC-Q101 Compliance

DESCRIPTION

RT3N22M is composite transistor built with two RT1N241 chips in SC-88 package.

FEATURE

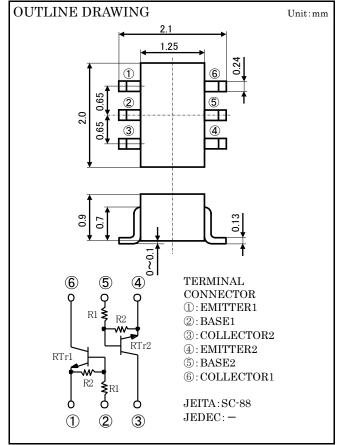
Silicon epitaxial type

Each transistor elements are independent.

Mini package for easy mounting

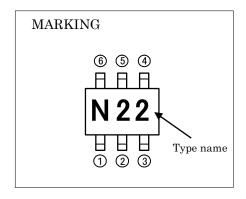
APPLICATION

Inverted circuit, Switching circuit, Interface circuit, Driver circuit



MAXIMUM RATING(Ta=25°C)(RTr1, RTr2 COMMON)

SYMBOL	PARAMETER	RATING	UNIT
Vcbo	Collector to Base voltage	50	V
V_{EBO}	Emitter to Base voltage	10	V
V_{CEO}	Collector to Emitter voltage	50	V
$V_{\rm IN}$	Input voltage	40	V
I_{C}	Collector current	100	mA
ICM	Peak Collector current	200	mA
PT	Total dissipation	200	mW
Tj	Junction temperature	+150	°C
T_{stg}	Storage temperature	-55~+150	°C



ELECTRICAL CHARACTERISTICS(Ta=25°C)(RTr1, RTr2 COMMON)

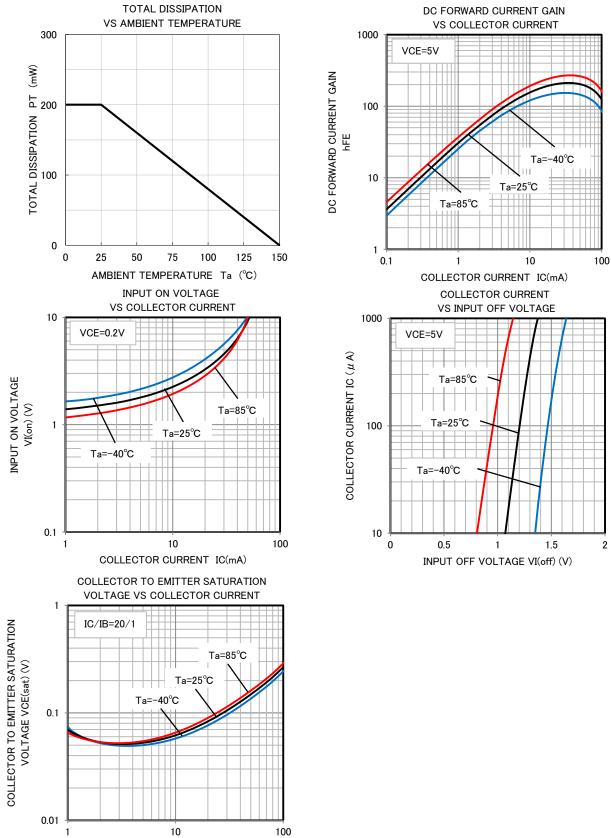
SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			TINITE
			MIN	TYP	MAX	UNIT
V(BR)CEO	Collector to Emitter breakdown voltage	I _C =100μA, R _{BE} =∞	50	_	_	V
Icbo	Collector cut off current	V_{CB} =50V, I_E =0	_	_	0.1	μA
I_{EBO}	Emitter cut off current	V_{EB} =5V, I_C =0	89	113	156	μA
$_{ m hFE}$	DC forward current gain	V_{CE} =5V, I_{C} =5mA	50	_	_	_
VCE(sat)	Collector to Emitter saturation voltage	$I_C=10$ mA, $I_B=0.5$ mA	_	0.1	0.3	V
$V_{\rm I(ON)}$	Input on voltage	Vce=0.2V, Ic=5mA	_	1.8	3.0	V
$V_{\rm I(OFF)}$	Input off voltage	V _{CE} =5V, I _C =100μA	0.8	1.1	_	V
R_1	Input resistor	_	16	22	28	kΩ
R_2/R_1	Resistor ratio	_	0.9	1.0	1.1	_
fT	Gain band width product	V _{CE} =6V, I _E =-10mA	_	200	_	MHz

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TYPICAL CHARACTERISTICS

(RTr1,RTr2 COMMON)



COLLECTOR CURRENT IC(mA)

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