RT3N11M-T150

Composite Transistor With Resistor For Switching Application Silicon Epitaxial Type

AEC-Q101 Compliance

DESCRIPTION

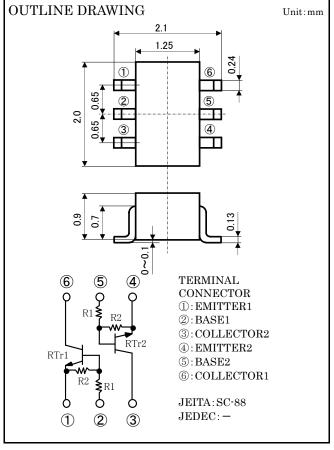
RT3N11M is composite transistor built with two RT1N141 chips in SC-88 package.

FEATURE

Silicon epitaxial type Each transistor elements are independent. Mini package for easy mounting

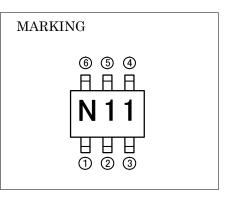
APPLICATION

Inverted circuit, Switching circuit, Interface circuit, Driver circuit



MAXIMUM RATING(Ta=25°C)(RTr1, RTr2 COMMON)

SYMBOL	PARAMETER	RATING	UNIT
VCBO	Collector to Base voltage	50	V
Vebo	Emitter to Base voltage	10	V
VCEO	Collector to Emitter voltage	50	V
V _{IN}	Input voltage	40	V
IC	Collector current	100	mA
ICM	Peak Collector current	200	mA
Рт	Total dissipation	200	mW
Tj	Junction temperature	+150	°C
T_{stg}	Storage temperature	-55~+150	°C



ELECTRICAL CHARACTERISTICS(Ta=25°C)(RTr1, RTr2 COMMON)

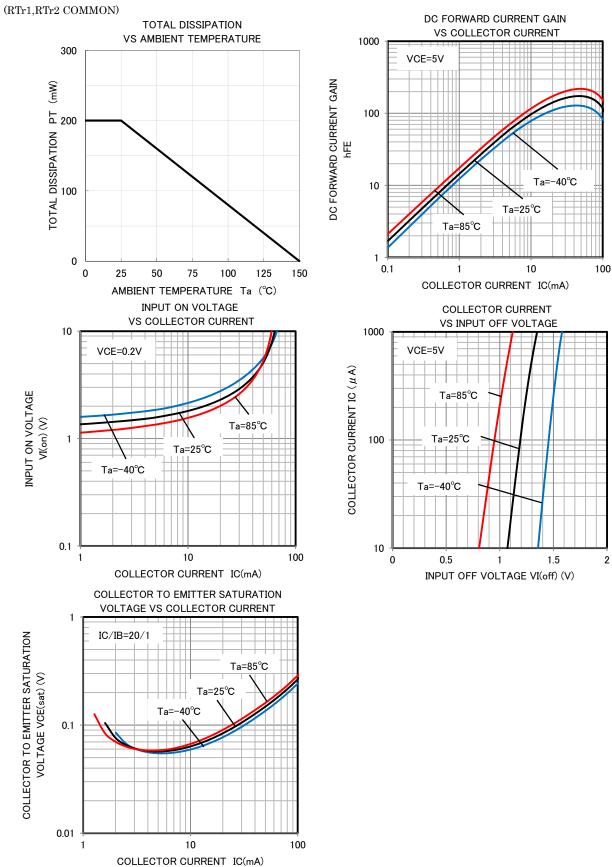
SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			
			MIN	TYP	MAX	UNIT
V(BR)CEO	Collector to Emitter breakdown voltage	I $_{\rm C}$ =100 μ A, R _{BE} = ∞	50	—	-	V
ICBO	Collector cut off current	V_{CB} =50V, I $_{E}$ =0	Ι	—	0.1	μA
IEBO	Emitter cut off current	V_{CB} =5V, I $_{C}$ =0	192	250	357	μA
hfe	DC forward current gain	V_{CE} =5V, I c=10mA	50	—	-	_
VCE(sat)	Collector to Emitter saturation voltage	I _c =10mA, I _b =0.5mA	-	0.1	0.3	V
V _{I(ON)}	Input on voltage	V _{CE} =0.2V, I _C =5mA	-	1.5	3.0	V
V _{I(OFF)}	Input off voltage	V _{CE} =5V, I _C =100 μ A	0.8	1.1	-	V
R1	Input resistor	-	7	10	13	kΩ
R_2/R_1	Resistor ratio	-	0.9	1.0	1.1	_
f_{T}	Gain band width product	$V_{CE}=6V$, I $_{E}=-10mA$	_	200	_	MHz

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TYPICAL CHARACTERISTICS

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