RT2P12M

Composite Transistor With Resistor For Switching Application Silicon PNP Epitaxial Type

DESCRIPTION

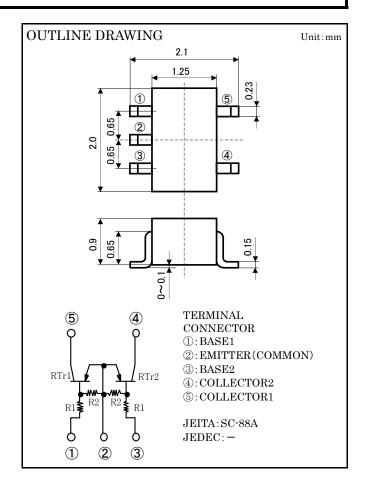
RT2P12M is composite transistor with built-in bias resistor.

FEATURE

Built-in bias resistor (R1=4.7k Ω , R2=47k Ω) Mini package for easy mounting

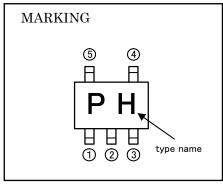
APPLICATION

Inverted circuit, Switching circuit, Interface circuit, Driver circuit



MAXIMUM RATING(Ta=25°C)(RTr1, RTr2 COMMON)

SYMBOL	PARAMETER	RATING	UNIT
Vcbo	Collector to Base voltage	-50	V
VEBO	Emitter to Base voltage	-6	V
$V_{\rm CEO}$	Collector to Emitter voltage	-50	V
$V_{\rm IN}$	Input voltage	-30	V
Ic	Collector current	-100	mA
I_{CM}	Peak Collector current	-200	mA
PT	Total dissipation	200	mW
Tj	Junction temperature	+150	လူ
$T_{ m stg}$	Storage temperature	-55~+150	လူ



$ELECTRICAL\ CHARACTERISTICS (Ta=25^{\circ}\text{C}) (RTr1,\ RTr2\ COMMON)$

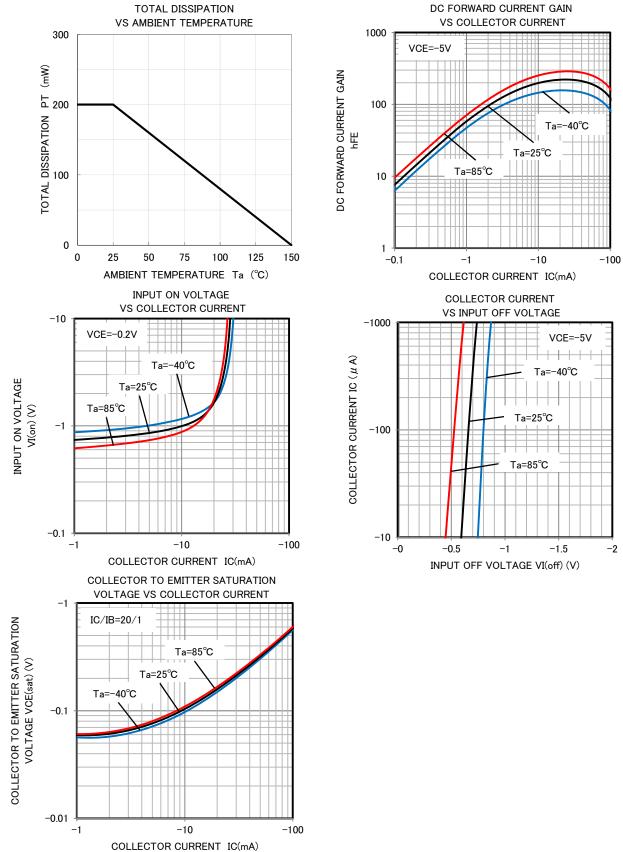
SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			TINITE
			MIN	TYP	MAX	UNIT
V(BR)CEO	Collector to Emitter break down voltage	I _C =-100 μ A, R _{BE} =∞	-50	_	_	V
Icbo	Collector cut off current	V _{CB} =-50V, I _E =0	_	_	-0.1	μΑ
IEBO	Emitter cut off current	V_{EB} =-5V, I_{C} =0	-73	-97	-140	μΑ
$_{ m hFE}$	DC forward current gain	V _{CE} =-5V, I _C =-10mA	80	_	_	_
VCE(sat)	Collector to Emitter saturation voltage	I _C =-10mA, I _B =-0.5mA	_	-0.1	-0.3	V
$V_{\rm I(ON)}$	Input on voltage	V _{CE} =-0.2V, I _C =-5mA	_	-0.8	-1.4	V
$V_{\rm I(OFF)}$	Input off voltage	V_{CE} =-5 V , I_{C} =-100 μ A	-0.4	-0.6	_	V
R ₁	Input resistor	_	3.3	4.7	6.1	kΩ
R_2/R_1	Resistor ratio	_	8	10	12	_
f_{T}	Gain band width product	V _{CE} =-6V, I _E =10mA	_	150	_	MHz

RT2P12M

Composite Transistor With Resistor For Switching Application Silicon PNP Epitaxial Type

TYPICAL CHARACTERISTICS

(RTr1,RTr2 COMMON)





Keep safety first in your circuit designs!

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