## RT2N17M

Composite Transistor With Resistor For Switching Application Silicon NPN Epitaxial Type

#### DESCRIPTION

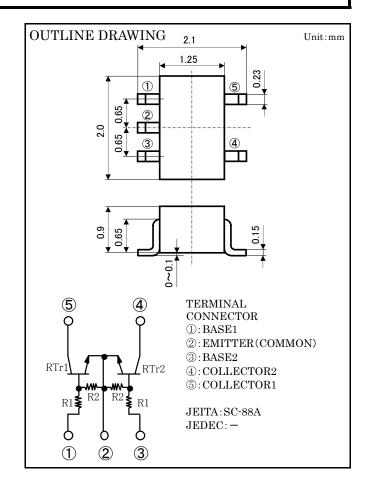
RT2N17M is composite transistor with built-in bias resistor.

### **FEATURE**

Built-in bias resistor (R1=47k $\Omega$ , R2=22k $\Omega$ ) Mini package for easy mounting

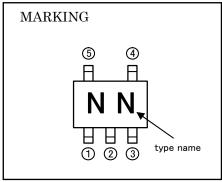
### APPLICATION

Inverted circuit, Switching circuit, Interface circuit, Driver circuit



## MAXIMUM RATING(Ta=25°C)(RTr1, RTr2 COMMON)

SYMBOL	PARAMETER	RATING	UNIT
Vcbo	Collector to Base voltage	50	V
VEBO	Emitter to Base voltage	10	V
$V_{CEO}$	Collector to Emitter voltage	50	V
$V_{\mathrm{IN}}$	Input voltage	40	V
Ic	Collector current	100	mA
Icm	Peak Collector current	200	mA
PT	Total dissipation	200	mW
Tj	Junction temperature	+150	°C
$T_{ m stg}$	Storage temperature	-55~+150	°C



## ${\bf ELECTRICAL\ CHARACTERISTICS} (Ta=25^{\circ}{\bf c}) (RTr1,\ RTr2\ COMMON)$

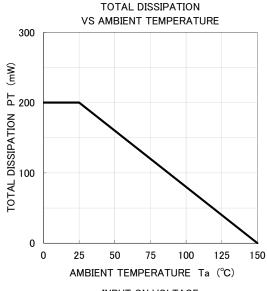
SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			TINITE
			MIN	TYP	MAX	UNIT
V(BR)CEO	Collector to Emitter breakdown voltage	I <sub>C</sub> =100 <i>μ</i> A, R <sub>BE</sub> =∞	50	_	_	V
Icbo	Collector cut off current	V <sub>CB</sub> =50V, I <sub>E</sub> =0	_	_	0.1	μΑ
IEBO	Emitter cut off current	$V_{EB}$ =5V, $I_C$ =0	54	72	105	μΑ
hfe	DC forward current gain	V <sub>CE</sub> =5V, I <sub>C</sub> =5mA	56	_	_	_
VCE(sat)	Collector to Emitter saturation voltage	$I_C=10$ mA, $I_B=0.5$ mA	_	_	0.3	V
$V_{\rm I(ON)}$	Input on voltage	V <sub>CE</sub> =0.2V, I <sub>C</sub> =5mA	_	2.6	6.3	V
$V_{\rm I(OFF)}$	Input off voltage	$V_{\text{CE}}$ =5 $V$ , $I_{\text{C}}$ =100 $\mu$ A	1.3	1.7	_	V
R <sub>1</sub>	Input resistor	_	33	47	61	kΩ
$R_2/R_1$	Resistor ratio	_	0.37	0.47	0.57	_
$f_{\mathrm{T}}$	Gain band width product	V <sub>CE</sub> =6V, I <sub>E</sub> =-10mA	_	200	_	MHz

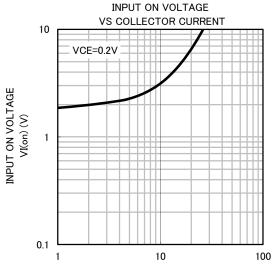
# **RT2N17M**

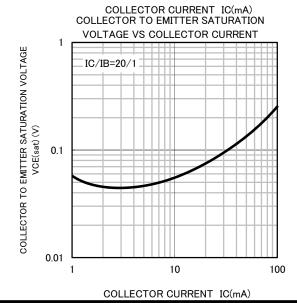
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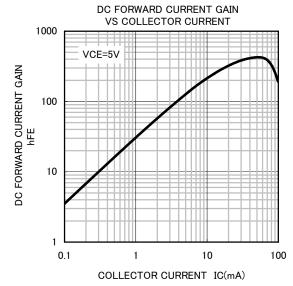
### TYPICAL CHARACTERISTICS

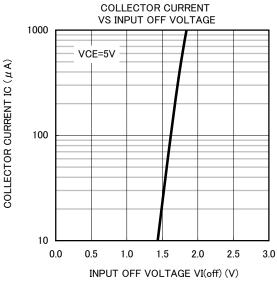
(Ta=25°C)(RTr1,RTr2 COMMON)













#### Keep safety first in your circuit designs!

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