RT2N08M

Composite Transistor With Resistor For Switching Application Silicon NPN Epitaxial Type

DESCRIPTION

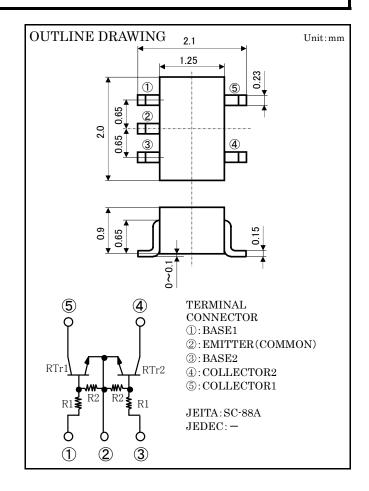
RT2N08M is composite transistor with built-in bias resistor.

FEATURE

Built-in bias resistor (R1= $2.2k\Omega$, R2= $10k\Omega$) Mini package for easy mounting

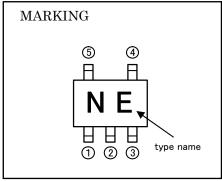
APPLICATION

Inverted circuit, Switching circuit, Interface circuit, Driver circuit



MAXIMUM RATING(Ta=25°C)(RTr1, RTr2 COMMON)

SYMBOL	PARAMETER	RATING	UNIT
Vcbo	Collector to Base voltage	50	V
VEBO	Emitter to Base voltage	6	V
$V_{\rm CEO}$	Collector to Emitter voltage	50	V
V_{IN}	Input voltage	12	V
Ic	Collector current	100	mA
I_{CM}	Peak Collector current	200	mA
PT	Total dissipation (Total)	200	mW
$T_{\rm j}$	Junction temperature	+150	ပ္
$T_{ m stg}$	Storage temperature	-55~+150	လူ

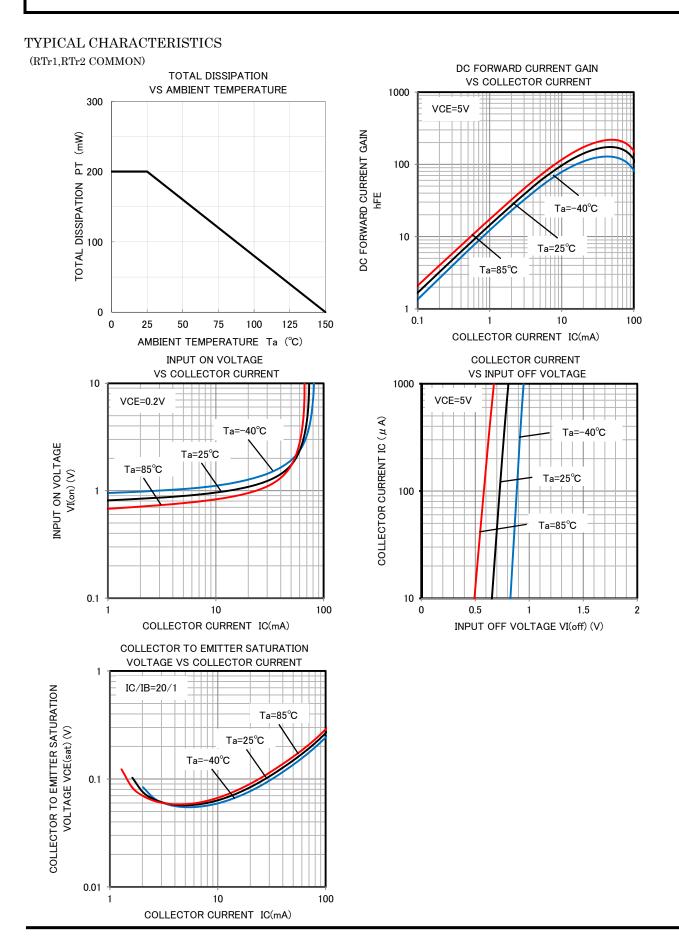


${\bf ELECTRICAL\ CHARACTERISTICS} (Ta=25^{\circ}{\bf c}) (RTr1,\ RTr2\ COMMON)$

SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			T 13 11m
			MIN	TYP	MAX	UNIT
V(BR)CEO	Collector to Emitter breakdown voltage	I _C =100 μ A, R _{BE} =∞	50	_	_	V
Icbo	Collector cut off current	V _{CB} =50V, I _E =0	_	_	0.1	μΑ
IEBO	Emitter cut off current	V_{EB} =5V, I_{C} =0	307	410	594	μΑ
hfe	DC forward current gain	V _{CE} =5V, I _C =10mA	33	_	_	_
VCE(sat)	Collector to Emitter saturation voltage	I _C =10mA, I _B =0.5mA	_	0.1	0.3	V
$V_{\rm I(ON)}$	Input on voltage	V _{CE} =0.2V, I _C =5mA	_	0.8	1.4	V
$V_{\rm I(OFF)}$	Input off voltage	$V_{CE}=5V, I_{C}=100 \muA$	0.5	0.7	_	V
R ₁	Input resistor	_	1.5	2.2	2.9	kΩ
R_2/R_1	Resistor ratio	_	3.8	4.7	5.6	_
f_{T}	Gain band width product	V _{CE} =6V, I _E =-10mA	_	200	_	MHz

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Keep safety first in your circuit designs!

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