RT2N03M

Composite Transistor With Resistor For Switching Application Silicon NPN Epitaxial Type

DESCRIPTION

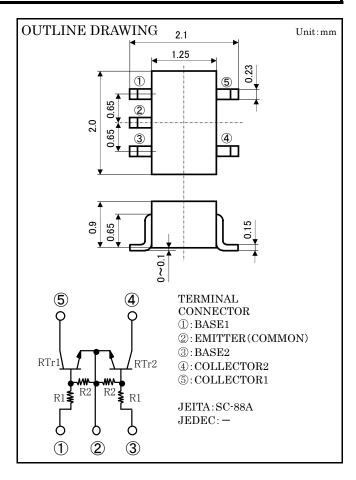
RT2N03M is composite transistor with built-in bias resistor.

FEATURE

Built-in bias resistor (R1=10k Ω , R2=10k Ω) Mini package for easy mounting

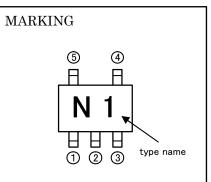
APPLICATION

Inverted circuit, Switching circuit, Interface circuit, Driver circuit



MAXIMUM RATING(Ta=25°C)(RTr1, RTr2 COMMON)

SYMBOL	PARAMETER	RATING	UNIT	
VCBO	Collector to Base voltage	50	V	
Vebo	Emitter to Base voltage	10	V	
VCEO	Collector to Emitter voltage	50	V	
$V_{\rm IN}$	Input voltage	40	V	
IC	Collector current	100	mA	
ICM	Peak Collector current	200	mA	
Рт	Total dissipation	200	mW	
Tj	Junction temperature	+150	°C	
$T_{\rm stg}$	Storage temperature	-55~+150	°C	



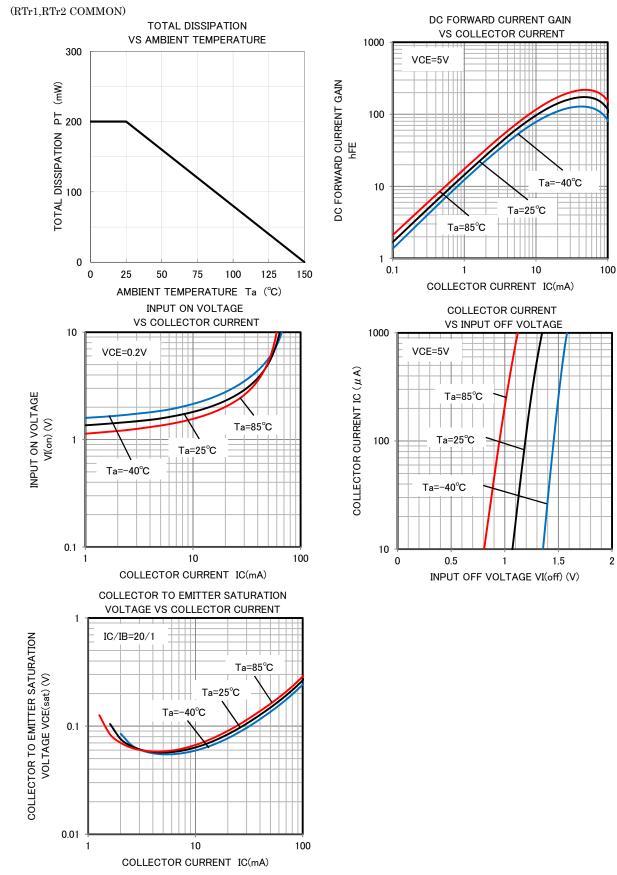
ELECTRICAL CHARACTERISTICS(Ta=25°C)(RTr1, RTr2 COMMON)

SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			
			MIN	TYP	MAX	UNIT
V(BR)CEO	Collector to Emitter breakdown voltage	$I_{C}=100 \ \mu A$, $R_{BE}=\infty$	50	—	_	V
ICBO	Collector cut off current	$V_{CB}=50V$, $I_{E}=0$	1	—	0.1	μA
IEBO	Emitter cut off current	$V_{CB}=5V$, $I_C=0$	192	250	357	μA
hfe	DC forward current gain	V _{CE} =5V, I _C =10mA	50	—	—	_
VCE(sat)	Collector to Emitter saturation voltage	Ic=10mA, IB=0.5mA	-	0.1	0.3	V
V _{I(ON)}	Input on voltage	V _{CE} =0.2V, I _C =5mA	1	1.5	3.0	V
$V_{\mathrm{I(OFF)}}$	Input off voltage	$V_{CE}=5V$, $I_C=100 \mu A$	0.8	1.1	—	V
R1	Input resistor	-	7	10	13	kΩ
R_2/R_1	Resistor ratio	-	0.9	1.0	1.1	_
f_{T}	Gain band width product	$V_{CE}=6V$, $I_{E}=-10mA$	_	200	_	MHz

ISAHAYA ELECTRONICS CORPORATION

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TYPICAL CHARACTERISTICS

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