RT1N441C-T150

OUTLINE DRAWING

Transistor With Resistor For Switching Application Silicon NPN Epitaxial Type

AEC-Q101 Compliance

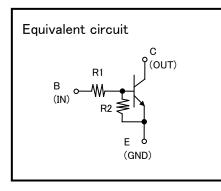
UNIT:mm

FEATURE

- •Built-in bias resistor (R1=47k Ω ,R2=47k Ω)
- •Mini package for easy mounting

APPLICATION

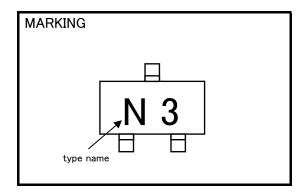
Inverted circuit, switching circuit, interface circuit, driver circuit.



2.8 0.65 1.5 0.65 0.95 1.90 2.8 0.95 3 0.8 0~0.1 Terminal Connector (1):Base JEITA: SC-59 (2): Emitter JEDEC: Similar to TO-236 ③:Collector

MAXIMUM RATING(Ta=25°C)

SYMBOL	PARAMETER	RATING	UNIT	
V _{CBO}	Collector to Base voltage	50	V	
V _{EBO}	Emitter to Base voltage	10	V	
V _{CEO}	Collector to Emitter voltage	50	V	
V _{IN}	Input voltage	40	V	
I _C	Collector current	100	mA	
I _{CM}	Peak Collector current	200	mA	
Pc	Collector dissipation	200	mW	
Tj	Junction temperature	+150	°C	
T _{stg}	Storage temperature	-55~+150	C°	

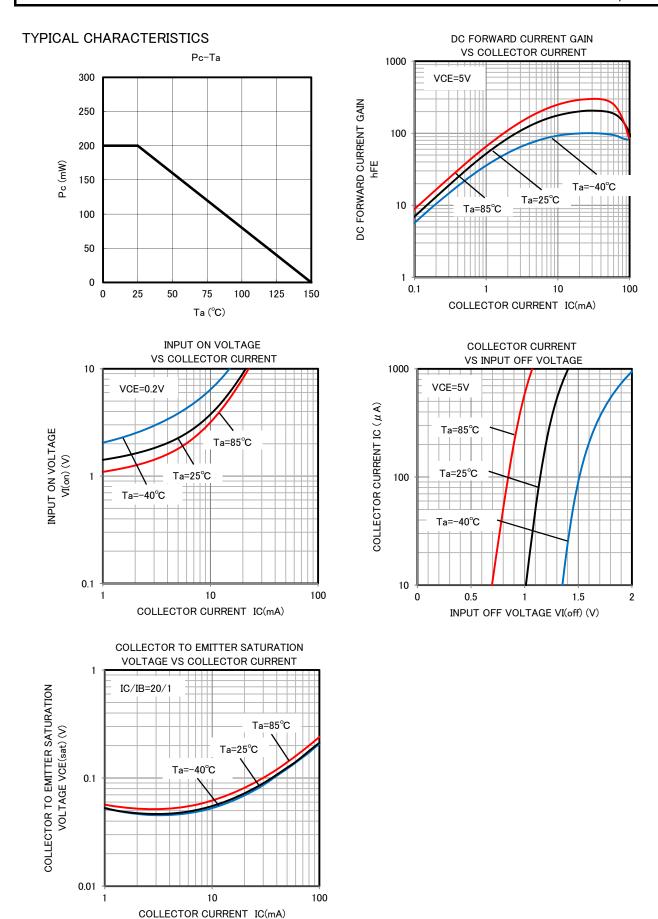


ELECTRICAL CHARACTERISTICS (Ta=25°C)

SYMBOL	PARAMETER	TEST CONDITION	LIMIT			UNIT
			MIN	TYP	MAX	UNIT
$V_{(BR)CEO}$	C to E breakdown voltage	$I_c=100 \mu$ A, $R_{BE}=\infty$	50		—	V
I _{CBO}	Collector cut off current	V_{CB} =50V, I_{E} =0			0.1	μA
I _{EBO}	Emitter cut off current	$V_{EB}=5V$, $I_{C}=0$	41	53	76	μA
h _{FE}	DC forward current gain	V _{CE} =5V、I _C =5mA	50		—	—
$V_{CE(sat)}$	C to E saturation voltage	I _c =10mA、I _B =0.5mA		0.1	0.3	V
V _{I(ON)}	Input on voltage	V _{CE} =0.2V、I _C =5mA	-	2.2	5.0	V
$V_{I(OFF)}$	Input off voltage	V _{CE} =5V, I _C =100 μ A	0.8	1.1	—	V
R1	Input resistor	-	33	47	61	kΩ
R2/R1	Resistor ratio	-	0.9	1.0	1.1	_
f _T	Gain band width product	V_{ce} =6V, I_{e} =-10mA	_	200	—	MHz

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