High Speed Switching Silicon P-channel MOSFET

DESCRIPTION

INJ0103AC1 is a Silicon P-channel MOSFET.

This product is most suitable for use such as portable machinery, because of low voltage drive and low on resistance.

FEATURE

- •Input impedance is high, and not necessary to consider a drive electric current.
- •Drive voltage −1.8V
- ·Low on Resistance.

RDS(ON)=0.67 Ω (Typ) @I_D=-400mA, V_{GS}=-4.5V

RDS(ON)=1.1 Ω (Typ) @I_D=-300mA, V_{GS}=-2.5V

RDS(ON)=1.6 Ω (Typ) @I_D=-10mA, V_{GS}=-1.8V

- ·High speed switching.
- ·Small package for easy mounting.

APPLICATION

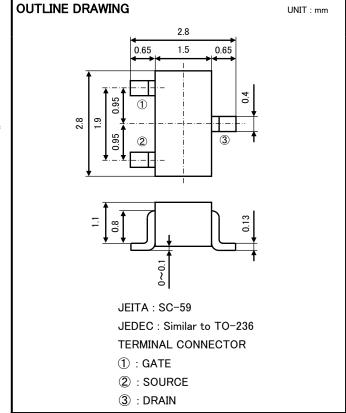
High speed switching, Analog switching

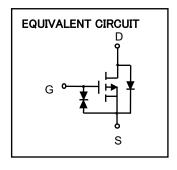
MAXIMUM RATINGS (Ta=25°C)

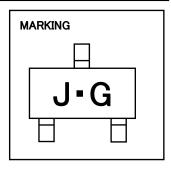
Parameter	Symbol	Rating	Unit	
Drain-Source Voltage	VDSS	-20	V	
Gate-Source Voltage	Vgss	±8	٧	
Drain Current (DC)	ĪD	-550	mA	
Drain Current (Pulse)	IDP	-2.2 ^{※1}	Α	
Total Power Dissipation	PD	200	mW	
		600**2	mW	
Channel Temperature	Tch	+150	°C	
Storage Temperature	Tstg	-55∼+150	°C	

- $\times 1: Pw \le 10 \mu s$, Duty cycle $\le 1\%$

ELECTRICAL CHARACTERISTICS (Ta=25°C)





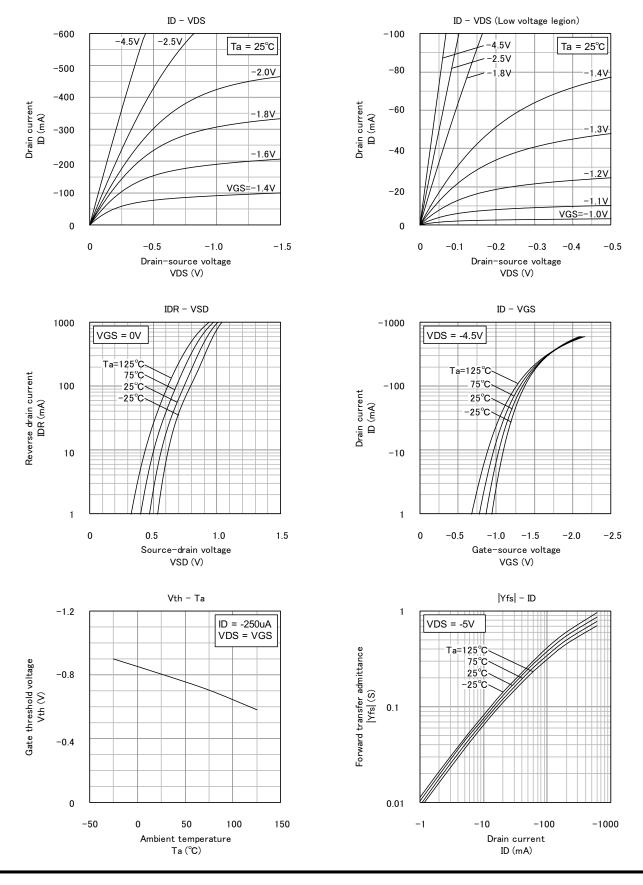


Parameter	Symbol	Test Condition	Limit			Unit
			Min	Тур	Max	Unit
Drain-Source Breakdown Voltage	V(BR)DSS	I _D =-100 \(\mu\)A, \(\mathbf{V}_{GS}=0\)V	-20	_	_	٧
Gate-Source Leak Current	Igss	$V_{GS}=\pm 5V$, $V_{DS}=0V$	_	_	±0.5	μA
Zero Gate Voltage Drain Current	Idss	V _{DS} =-20V, V _{GS} =0V	_	_	-1.0	μA
Gate Threshold Voltage	Vth	I _D =-250 μA, V _{DS} = V _{GS}	-0.4	_	-1.3	٧
Forward Transfer Admittance	Yfs	V _{DS} =-5V, I _D =-200mA	-	400	_	mS
Static Drain-Source On-State Resisitance	RDS(ON)	I _D =-400mA, V _{GS} =-4.5V	-	0.67	_	Ω
		$I_D = -300 \text{mA}, V_{GS} = -2.5 \text{V}$	_	1.1	_	
		I _D =-10mA, V _{GS} =-1.8V	_	1.6	_	
Input Capacitance	Ciss	\\ - 10\\ \\ - 2\\ (-1M)	_	57	_	pF
Output Capacitance	Coss	V_{DS} =-10V, V_{GS} =0V, f=1MHz	_	20	_	
Swithing Time	ton	V _{DD} =-10V, I _D =-400mA	_	42	_	ns
	toff	V _{GS} =0 ~ −5V	-	140	_	

INJ0103AC1

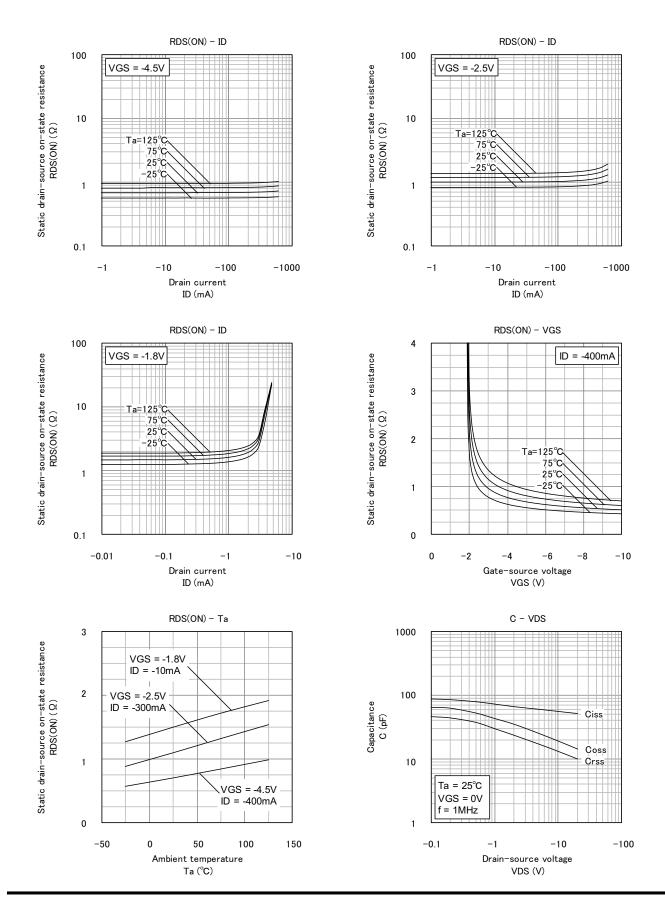
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TYPICAL CHARACTERISTICS



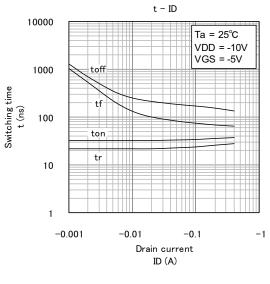
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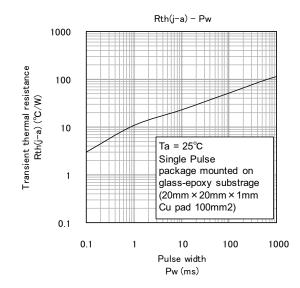
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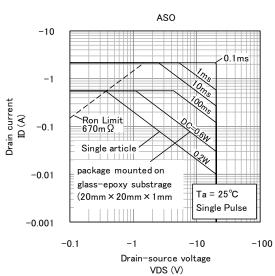


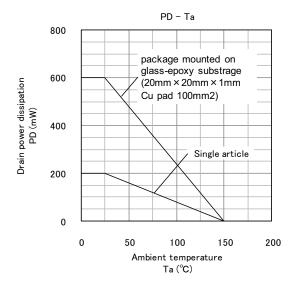
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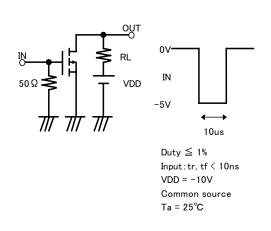


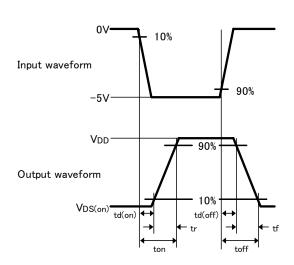






Switching time test condition







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