# 2SA2027

## FOR LOW FREQUENCY AMPLIFY APPLICATION SILICON PNP EPITAXIAL TYPE

### DESCRIPTION

2SA2027 is a silicon PNP epitaxial type transistor. It is designed with high voltage application.

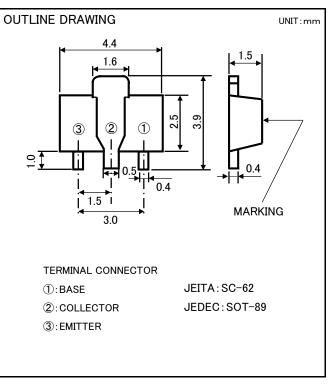
#### FEATURE

•Small collector to emitter saturation voltage

- VCE(sat)=-0.5V max(@IC=-100mA/IB=-10mA)
- Small package for easy mounting

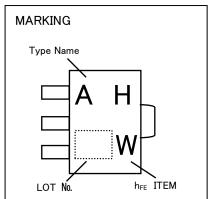
#### APPLICATION

Hybrid IC, DC-DC converter



#### MAXIMUM RATING(Ta=25°C)

SYMBOL	PARAMETER	RATING	
V <sub>CBO</sub>	Collector to Base voltage	re -300	
V <sub>EBO</sub>	Emitter to Base voltage	-7	V
V <sub>CEO</sub>	Collector to Emitter voltage	-300	V
Ic	Collector current	-100	mA
Pc	Collector dissipation(Ta=25°C)	500	mW
Tj	Junction temperature	+150	°C
$T_{stg}$	Storage temperature	-55~+150	°C



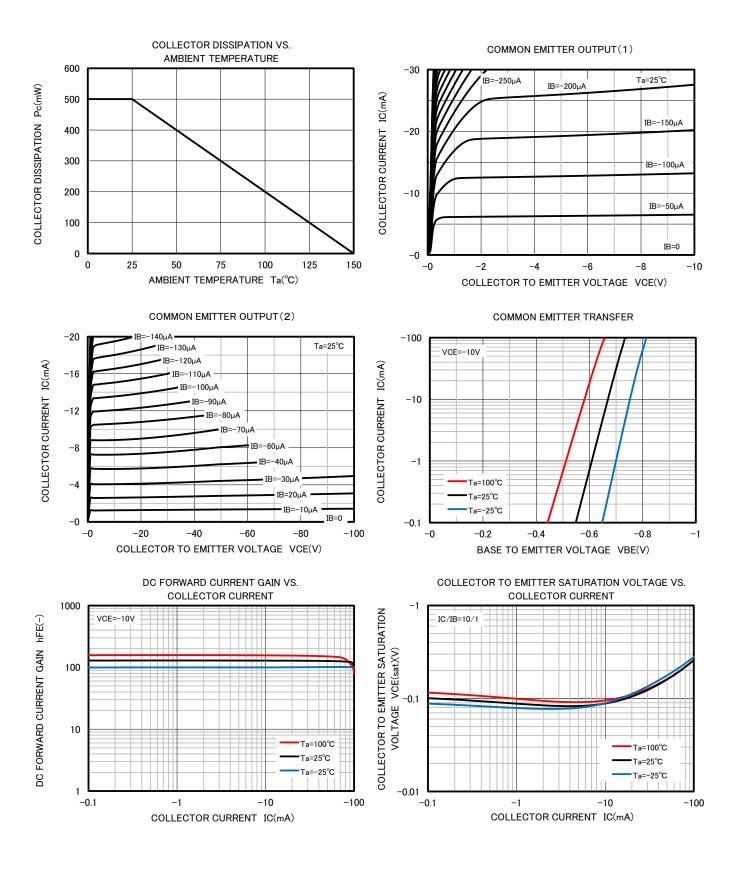
#### ELECTRICAL CHARACTERISTICS (Ta=25°C)

SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	UNIT
V <sub>(BR)CBO</sub>	C to B breakdown voltage	$I_c$ =-50 $\mu$ A, $I_E$ =0mA	-300	-	-	V
V <sub>(BR)EBO</sub>	E to B breakdown voltage	$I_{E}$ =-50 $\mu$ A, $I_{C}$ =0mA	-7	-	-	V
V <sub>(BR)CEO</sub>	C to E breakdown voltage	I <sub>c</sub> =−1mA, R <sub>BE</sub> =∞	-300	-	-	V
I <sub>CBO</sub>	Collector cut off current	$V_{CB}$ =-300V, I <sub>E</sub> =0mA	-	-	-0.5	μA
Іево	Emitter cut off current	V <sub>EB</sub> =-5V, Ic=0mA	-	-	-0.5	μA
hfe	DC forward current gain	Vce=-10V, Ic=-10mA	50	-	305	-
$V_{CE(sat)}$	C to E saturation voltage	Ic=-100mA, I <sub>B</sub> =-10mA	-	-	-0.5	V
fT	Gain bandwidth product	$V_{CE}$ =-6V, I <sub>E</sub> =10mA	-	40	-	MHz
Cob	Collector output capacitance	$V_{ce}$ =-6V, I <sub>e</sub> =0mA, f=1MHz	-	3.0	_	pF

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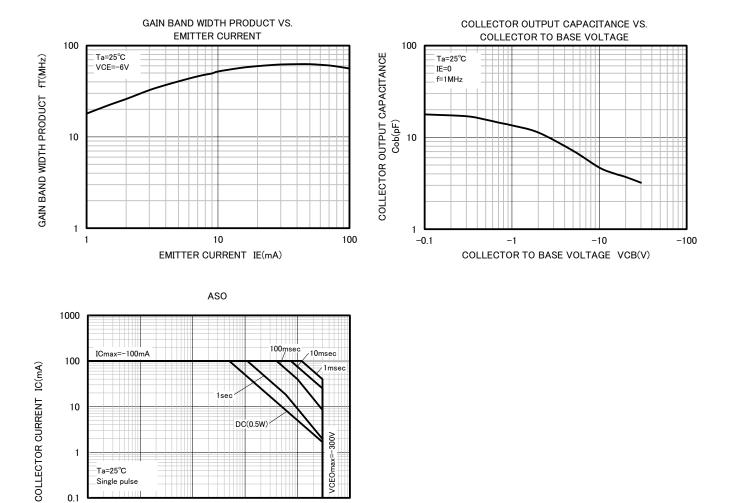
#### TYPICIAL CHARACTERISTICS



ISAHAYA ELECTRONICS CORPORATION

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1se

COLLECTOR TO EMITTER VOLTAGE VCE(V)

1

DC(0.5W)

10

-300V

VCEOmax=-

1000

100

10

1

0.1 0.01

Ta=25°C Single pulse

0.1

#### Keep safety first in your circuit designs!

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